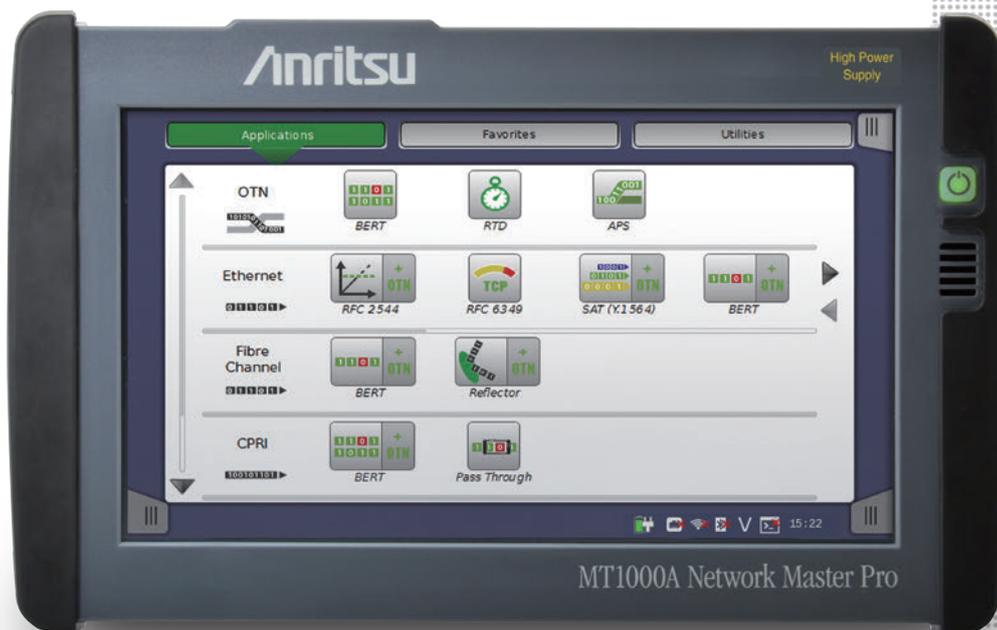


Anritsu envision : ensure

Network Master™ Series

Network Master Pro MT1000A

10G Multirate Module	MU100010A
100G Multirate Module	MU100011A
High Performance GPS Disciplined Oscillator	MU100090A



Network Master Pro MT1000A Overview

Redefining Transport Testing

Network standards for transport tests are becoming more diverse, such as Ethernet, OTN, SDH/SONET, CPRI, PTP, Fibre Channel, etc. Additionally, speeds are increasing with 100G/25G Ethernet and 16G Fibre Channel development, and networks are continuing to evolve. With an easily configured modular design to support changing network standards and an easy-to-use GUI, the Network Master Pro MT1000A is perfect for speedy I&M of wide-area networks.

Evolving Networks and Required Test Equipment

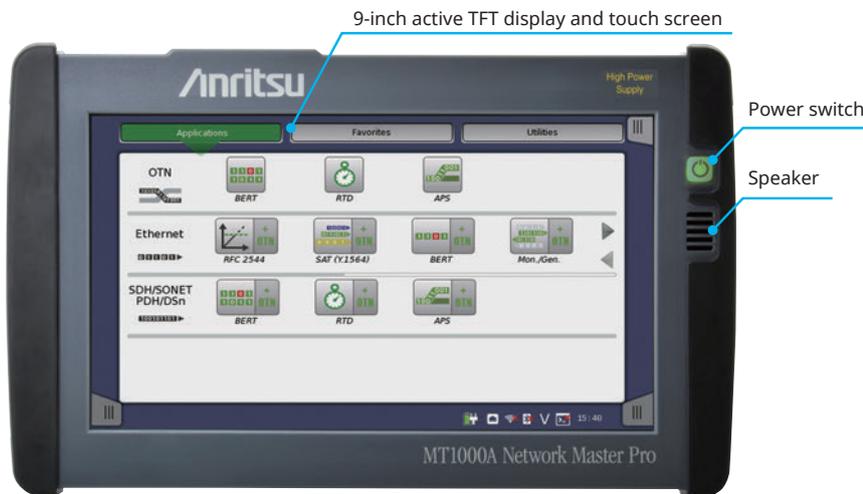
Core and metro networks are progressing to 100G interfaces. In addition, operators are also examining adoption of new and more types of client interfaces. Onsite network I&M work requires cost-effective support for diversifying services at speeds from 1G to 100G with a field tester that improves work efficiency.

- All-in-one transport tester
 - supports testing from 1.5 Mbps to 100 Gbps
- Easy and intuitive GUI
- WLAN*/Bluetooth*/LAN connectivity
- PDF, CSV and XML report generation for documenting test results
- Remote operation
- Remote control
- Compact, lightweight design for maximum field portability
- Modular platform ensuring maximum return on investment

*: Available for certified countries and regions including USA, Canada, Japan and EU countries. Please visit the Anritsu web site for updated information. The Bluetooth® mark and logos are registered trademarks of Bluetooth SIG, Inc.

Connector Panel Overview

Front View

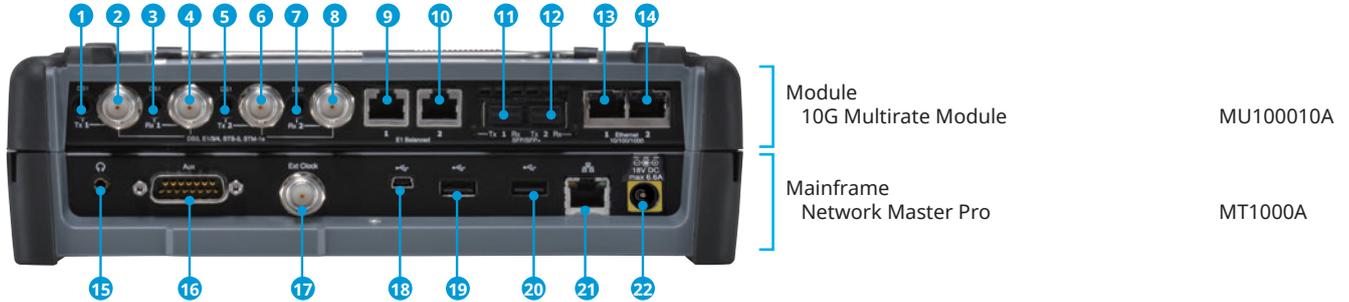


Just a fraction larger than 9-inch screen

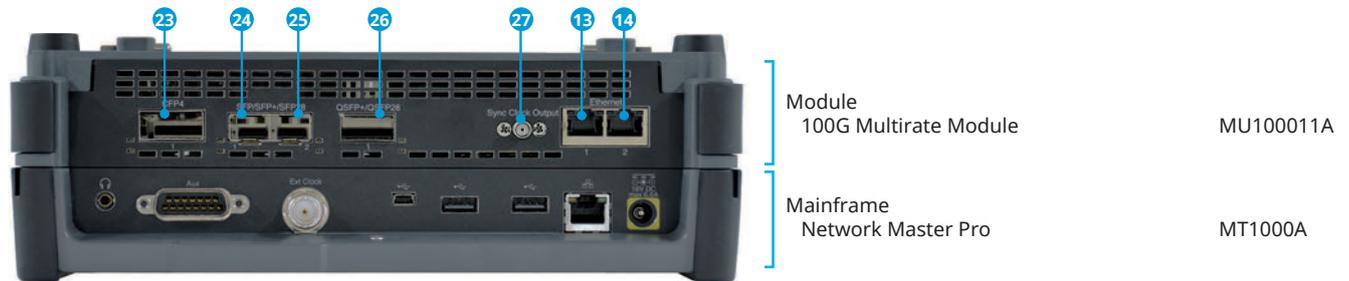
Connector Panel Overview

Top View

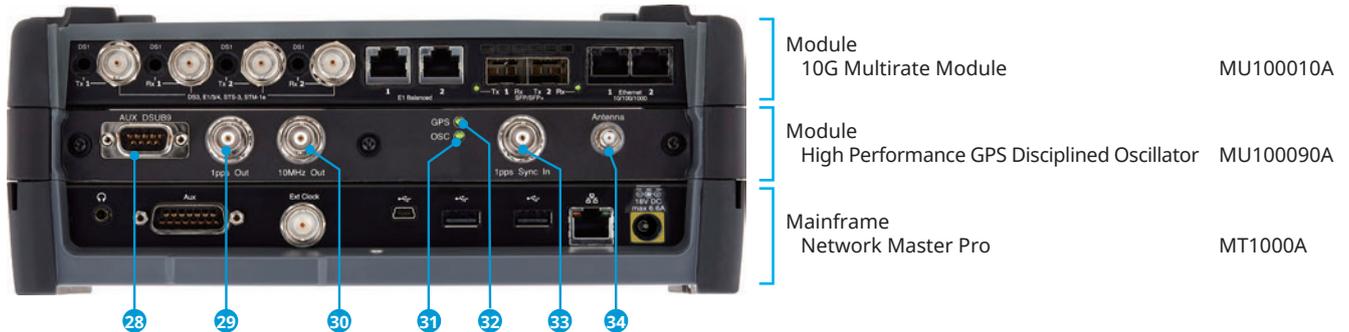
10G Multirate Module MU100010A



100G Multirate Module MU100011A



High Performance GPS Disciplined Oscillator Configuration



- | | |
|--|--|
| <ul style="list-style-type: none"> 1 Port 1, Tx Bantam (DS1) 2 Port 1, Tx BNC (E1, E3, E4, DS3, STM-1e, STS-3) 3 Port 1, Rx Bantam (DS1) 4 Port 1, Rx BNC (E1, E3, E4, DS3, STM-1e, STS-3) 5 Port 2, Tx Bantam (DS1) 6 Port 2, Tx BNC (E1, E3, E4, DS3, STM-1e, STS-3) 7 Port 2, Rx Bantam (DS1) 8 Port 2, Rx BNC (E1, E3, E4, DS3, STM-1e, STS-3) 9 Port 1, Tx/Rx RJ48 (E1 balanced) 10 Port 2, Tx/Rx RJ48 (E1 balanced) 11 Port 1, Tx/Rx SFP/SFP+ (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET optical) 12 Port 2, Tx/Rx SFP/SFP+ (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET optical) 13 Port 1, Tx/Rx RJ45 (Ethernet electrical) 14 Port 2, Tx/Rx RJ45 (Ethernet electrical) 15 Audio (3.5Ø: CTIA Standard) 16 AUX (D-SUB 15 pin) 17 Clock Input | <ul style="list-style-type: none"> 18 USB Mini-B 19 USB A 20 USB A 21 Ethernet Service Interface 22 DC Input (18 Vdc) 23 Port1, Tx/Rx CFP4 (OTN, Ethernet) 24 Port1, Tx/Rx SFP/SFP+/SFP28 (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET) 25 Port2, Tx/Rx SFP/SFP+/SFP28 (OTN, Ethernet, CPRI/OBSAI, Fibre Channel, SDH/SONET) 26 Port1, Tx/Rx QSFP+/QSFP28 (OTN, Ethernet) 27 Port1, Sync. Clock Output 28 AUX (D-SUB 9 pin) 29 1 pps Output (REF) 30 10 MHz Output 31 OCS LED 32 GPS received LED 33 1 pps Sync In 34 Antenna Input |
|--|--|

Network Master Pro MT1000A Mainframe Specifications

User Interfaces	
Display	9-inch active TFT display (800 × 480 pixels) and touch screen
Supported Languages	English, Chinese, Japanese, French, Russian, Spanish

Service Interfaces	
USB Data Interface	MT1000A operates as host: USB 2.0 type A (2 ports) MT1000A operates as device: USB 2.0 type Mini-B (1 port)
Ethernet Interface	Ethernet 10M/100M/1000M, Connector: RJ45
WLAN Interface*1	IEEE 802.11 b/g/n
Bluetooth Interface*1	Bluetooth 2.1 +EDR

*1: Available for certified countries and regions including USA, Canada, Japan and EU countries. Please visit the Anritsu web site for updated information.

Other Interfaces	
Audio Interface	For connection of CTIA Standard head set Connector: 3.5-mm diameter jack
AUX Connector	For connection of optional G0325A GPS receiver With MT1000A-005: For connection of Optional MU100090A
Built-in Loudspeaker	Monitors speech of voice channel Output level: user-controlled from user Interface
Internal Clock	Accuracy: ±4.6 ppm or less, STRATUM3 compliant
Ext. Clock Input	For connection of external clock signals: SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps) or 2.048 MHz TTL signal in accordance with ITU-T G.703, 10 MHz Connector: BNC (50Ω)

Miscellaneous		
Battery	10.8 V rechargeable and replaceable intelligent Li-ion battery Operating time: 1.5 hours (typ., in case of 100 GbE) Charging time: 6 hours (Max.) Remaining capacity indication: %	
Mains Adapter	Input: 100 V(ac) to 240 V(ac), 50 Hz/60 Hz Output: 18 V(dc), 3.62 A (max.) Power Consumption: ≤65 W With MT1000A-006*2 Input: 100 V(ac) to 240 V(ac), 50 Hz/60 Hz Output: 18 V(dc), 6.6 A (max.) Power Consumption: ≤120 W	
Dimensions and Mass	257 (W) × 164 (H) × 82 (D) mm (Exclude Projection, MT1000A + MU100010A) 257 (W) × 164 (H) × 89 (D) mm (Exclude Projection, MT1000A + MU100011A) 2.7 kg (including MT1000A, MU100010A and battery) 2.7 kg (including MT1000A, MU100011A and battery)	
Environmental	Temperature Operating : 0° to +50°C (non-condensing) Charging: 0° to +40°C (non-condensing) Storage: -30° to +60°C (non-condensing, without battery or AC adapter) -20° to +50°C (non-condensing, with battery and AC adapter) Humidity Operating: ≤85% RH (non-condensing) Storage and Transportation: ≤90% RH (non-condensing, without battery and AC adapter) Storage and Transportation: ≤85% RH (non-condensing, with battery and AC adapter)	
CE	EMC	EN61326-1, EN61000-3-2
	LVD	EN61010-1
	RoHS	EN50581

*2: MT1000A-006 is required for MU100011A.



Network Master Pro MT1000A with MT1000A-006
(A yellow border is printed around DC Input.)



Network Master Pro MT1000A

10G Multirate Module MU100010A Specifications

Test Interface																																																																																																																												
Test Port	SFP/SFP+: 2 slots SFF-8431, SFF-8472 compliant, IEEE 802.3ae-2002, IEEE 802.3-2008 compliant RJ45: 2 sockets IEEE 802.3-2008 10BASE-T, 100BASE-TX, 1000BASE-T compliant Auto MDI-X 10 Mbps/100 Mbps full/half duplex, 1000 Mbps full duplex BNC: 2 ports (75Ω) ITU-T G.703 compliant RJ48: 2 sockets ITU-T G.703 compliant RTT Bantam: 2 ports ANSI DS1.102 compliant																																																																																																																											
Bit Rate*1	<table border="1"> <thead> <tr> <th>Standard</th> <th>Bit Rate</th> <th>Interfaces</th> </tr> </thead> <tbody> <tr><td>10BASE-T</td><td>12.5 Mbps</td><td>RJ45</td></tr> <tr><td>100BASE-TX</td><td>125 Mbps</td><td>RJ45</td></tr> <tr><td>1000BASE-T</td><td>1.25 Gbps</td><td>RJ45</td></tr> <tr><td>100BASE-XX</td><td>125 Mbps</td><td>SFP</td></tr> <tr><td>1000BASE-XX</td><td>1.25 Gbps</td><td>SFP</td></tr> <tr><td>10GBASE-XX</td><td>10.3125 Gbps</td><td>SFP+</td></tr> <tr><td>STM-1/OC-3</td><td>155.52 Mbps</td><td>SFP</td></tr> <tr><td>STM-4/OC-12</td><td>622.08 Mbps</td><td>SFP</td></tr> <tr><td>STM-16/OC-48</td><td>2.48832 Mbps</td><td>SFP</td></tr> <tr><td>STM-64/OC-192</td><td>9.95328 Mbps</td><td>SFP+</td></tr> <tr><td>OTU1</td><td>2.666057143 Gbps</td><td>SFP</td></tr> <tr><td>OTU2</td><td>10.70922532 Gbps</td><td>SFP+</td></tr> <tr><td>OTU1e</td><td>11.04910714 Gbps</td><td>SFP+</td></tr> <tr><td>OTU2e</td><td>11.09572785 Gbps</td><td>SFP+</td></tr> <tr><td>OTU1f</td><td>11.27008929 Gbps</td><td>SFP+</td></tr> <tr><td>OTU2f</td><td>11.31764241 Gbps</td><td>SFP+</td></tr> <tr><td>1GFC</td><td>1.0625 Gbps</td><td>SFP</td></tr> <tr><td>2GFC</td><td>2.125 Gbps</td><td>SFP</td></tr> <tr><td>4GFC</td><td>4.25 Gbps</td><td>SFP</td></tr> <tr><td>8GFC</td><td>8.5 Gbps</td><td>SFP+</td></tr> <tr><td>10GFC</td><td>10.51875 Gbps</td><td>SFP+</td></tr> </tbody> </table> <table border="1"> <thead> <tr> <th>Standard</th> <th>Bit Rate</th> <th>Interfaces</th> </tr> </thead> <tbody> <tr><td>E1</td><td>2.048 Mbps</td><td>RJ48, BNC</td></tr> <tr><td>E3</td><td>34.368 Mbps</td><td>BNC</td></tr> <tr><td>E4</td><td>139.264 Mbps</td><td>BNC</td></tr> <tr><td>DS1</td><td>1.544 Mbps</td><td>RTT Bantam</td></tr> <tr><td>DS3</td><td>44.736 Mbps</td><td>BNC</td></tr> <tr><td>STM-1e/STS-3</td><td>155.52 Mbps</td><td>BNC</td></tr> <tr><td>CPRI1</td><td>614.4 Mbit/s</td><td>SFP</td></tr> <tr><td>CPRI2</td><td>1228.8 Mbit/s</td><td>SFP</td></tr> <tr><td>CPRI3</td><td>2457.6 Mbit/s</td><td>SFP, SFP+</td></tr> <tr><td>CPRI4</td><td>3072.0 Mbit/s</td><td>SFP, SFP+</td></tr> <tr><td>CPRI5</td><td>4915.2 Mbit/s</td><td>SFP+</td></tr> <tr><td>CPRI6</td><td>6144.0 Mbit/s</td><td>SFP+</td></tr> <tr><td>CPRI7</td><td>9830.4 Mbit/s</td><td>SFP+</td></tr> <tr><td>CPRI8</td><td>10137.6 Mbit/s</td><td>SFP+</td></tr> <tr><td>OBSAI 1x</td><td>768 Mbit/s</td><td>SFP</td></tr> <tr><td>OBSAI 2x</td><td>1536 Mbit/s</td><td>SFP, SFP+</td></tr> <tr><td>OBSAI 4x</td><td>3072 Mbit/s</td><td>SFP, SFP+</td></tr> <tr><td>OBSAI 8x</td><td>6144 Mbit/s</td><td>SFP+</td></tr> </tbody> </table>	Standard	Bit Rate	Interfaces	10BASE-T	12.5 Mbps	RJ45	100BASE-TX	125 Mbps	RJ45	1000BASE-T	1.25 Gbps	RJ45	100BASE-XX	125 Mbps	SFP	1000BASE-XX	1.25 Gbps	SFP	10GBASE-XX	10.3125 Gbps	SFP+	STM-1/OC-3	155.52 Mbps	SFP	STM-4/OC-12	622.08 Mbps	SFP	STM-16/OC-48	2.48832 Mbps	SFP	STM-64/OC-192	9.95328 Mbps	SFP+	OTU1	2.666057143 Gbps	SFP	OTU2	10.70922532 Gbps	SFP+	OTU1e	11.04910714 Gbps	SFP+	OTU2e	11.09572785 Gbps	SFP+	OTU1f	11.27008929 Gbps	SFP+	OTU2f	11.31764241 Gbps	SFP+	1GFC	1.0625 Gbps	SFP	2GFC	2.125 Gbps	SFP	4GFC	4.25 Gbps	SFP	8GFC	8.5 Gbps	SFP+	10GFC	10.51875 Gbps	SFP+	Standard	Bit Rate	Interfaces	E1	2.048 Mbps	RJ48, BNC	E3	34.368 Mbps	BNC	E4	139.264 Mbps	BNC	DS1	1.544 Mbps	RTT Bantam	DS3	44.736 Mbps	BNC	STM-1e/STS-3	155.52 Mbps	BNC	CPRI1	614.4 Mbit/s	SFP	CPRI2	1228.8 Mbit/s	SFP	CPRI3	2457.6 Mbit/s	SFP, SFP+	CPRI4	3072.0 Mbit/s	SFP, SFP+	CPRI5	4915.2 Mbit/s	SFP+	CPRI6	6144.0 Mbit/s	SFP+	CPRI7	9830.4 Mbit/s	SFP+	CPRI8	10137.6 Mbit/s	SFP+	OBSAI 1x	768 Mbit/s	SFP	OBSAI 2x	1536 Mbit/s	SFP, SFP+	OBSAI 4x	3072 Mbit/s	SFP, SFP+	OBSAI 8x	6144 Mbit/s	SFP+
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Dimensions and Mass	257 (W) × 163 (H) × 38.5 (D) mm, ≤1.1 kg						
Environmental	Temperature and Humidity Operating: 0° to +50°C, ≤80% RH (non-condensing) Storage: -30° to +60°C, ≤80% RH (non-condensing)						
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LVD	EN61010-1						
RoHS	EN50581						
Laser Safety*3	IEC 60825-1: 2007 CLASS 1 21CFR1040.10 and 1040.11*2						

*1: The frequency accuracy depends on the accuracy of the MT1000A internal clock or the external clock of MT1000A.

Refer to the external interfaces in MT1000A specifications.

*2: Excludes deviations caused by conformance to Laser Notice No. 50 dated June 24, 2007

*3: Safety measures for laser products

This product complies with optical safety standards in 21CFR1040.10, 1040.11 and IEC 60825-1; the following descriptive labels are affixed to the product.



10G Multirate Module MU100010A

100G Multirate Module MU100011A*1

Test Interface																																																																																																																																					
Test Port	<p>RJ45: 2 sockets IEEE 802.3-2008 10BASE-T, 100BASE-TX, 1000BASE-T compliant Auto MDI-X 10 Mbps/100 Mbps Full/Half duplex, 1000 Mbps Full duplex</p> <p>SFP/SFP+/SFP28: 2 slots SFF-8431, SFF-8472, SFF-8665 compliant, IEEE 802.3ae-2002, IEEE 802.3-2008, IEEE 802.3by compliant</p> <p>QSFP+/QSFP28: 1 port SFF-8436, SFF-8472 compliant, IEEE 802.3ba-2010 compliant</p> <p>CFP4: 1 port CFP MSA CFP4 Hardware Specification, Rev. 1.1 compliant CFP MSA Management Interface Specification V2.2 R06a compliant(However, MSA 100GLH is not support) IEEE 802.3ba-2010 compliant</p>																																																																																																																																				
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Sync Clock Output	<p>Frequency Select 1/8 or 1/16 against the bit rate of the data lane. 100 GigE: CAUI4 25 GigE: 25GAUI OTU4: OTL 4.4 Level: 250 mVp-p (min.), 550 mVp-p (max.) Termination: 50Ω/AC (Single ended) Connector: SMA</p>																																																																																																																																				

Miscellaneous		
Dimensions and Mass	257 (W) × 163 (H) × 48 (D) mm, ≤1.1 kg	
Environmental	<p>Temperature and Humidity Operating: 0° to +45°C, ≤85% RH (non-condensing) Storage: -30° to +60°C, ≤90% RH (non-condensing)</p>	
CE	EMC	EN61326-1, EN61000-3-2
	LVD	EN61010-1
	RoHS	EN50581
Laser Safety*3	<p>IEC 60825-1:2007 Class 1M CFP4: 100GBASE-SR4 QSFP+: 40GBASE-SR4 QSFP28: 100GBASE-SR4 SFP28: 25GBASE-SR</p> <p>IEC 60825-1:2007 Class 1 CFP4: 100GBASE-LR4 QSFP+: 40GBASE-LR4 QSFP28: 100GBASE-LR4 SFP28: 25GBASE-LR</p> <p>SFP+: 1000BASE-SX/LX/ZX, 10GBASE-LR/ER/ZR SFP: 4G FC (SX), 4G FC (LX), 4G FC (EX), OC-48 LR-1/STM L-16.1, OC-48 LR-2/STM L-16.2, 100BASE-FX/LX FDA 21CFR1040.10 and 1040.11*4</p>	

*1: MT1000A-006 is required for MU100011A.

*2: The frequency accuracy depends on the accuracy of the MT1000A internal clock or the external clock of MT1000A.

Refer to the external interfaces in MT1000A specifications.

*3: Safety measures for laser products

This product complies with optical safety standards in 21CFR1040.10, 1040.11 and IEC 60825-1; the following descriptive labels are affixed to the product.

*4: Excludes deviations caused by conformance to Laser Notice No. 50 dated June 24, 2007



THIS PRODUCT COMPLIES WITH 21 CFR 1040.10 AND 1040.11 EXCEPT FOR DEVIATIONS PURSUANT TO LASER NOTICE NO. 50, DATED JUNE 24, 2007



100G Multirate Module MU100011A

High Performance GPS Disciplined Oscillator MU100090A Specifications

Test Interface	
Internal Oscillator	Rubidium atomic clock Aging <ul style="list-style-type: none"> • 2.5×10^{-11}/Day • 1×10^{-10}/Month • 1×10^{-9}/Year
Input/Output Interface	10 MHz External Clock Output <ul style="list-style-type: none"> • Connector: BNC Jack • Level: TTL 50Ω 1 PPS Reference Output <ul style="list-style-type: none"> • Connector: BNC Jack • Level: TTL 50Ω • Holdover: 300 ns/10,000 seconds* 1 PPS Sync Input <ul style="list-style-type: none"> • Connector: BNC Jack • Level: TTL 50Ω • Input Frequency Range: ± 10 ppb GPS Antenna Input <ul style="list-style-type: none"> • Connector: SMA Jack • Feeding Antenna: +3.3 V, +5 V (Selectable, 50 mA max.) AUX <ul style="list-style-type: none"> • Connector: D-SUB 9 pin • Usage: TOD (Time of Day) output (NMEA0184), Connection to MT1000A with J1705A
GPS Signal Receive Specification	Supported Signal <ul style="list-style-type: none"> • GPS L1 C/A code Number of Receivable Channels <ul style="list-style-type: none"> • 50 Time Accuracy to UTC <ul style="list-style-type: none"> • ± 45 ns rms*
LED	GPS <ul style="list-style-type: none"> • GPS signal received (On: Tracking over four satellites) OCS <ul style="list-style-type: none"> • Oscillator lock (On: Locking)

Miscellaneous		
Dimensions and Mass	257 (W) × 163 (H) × 25 (D) mm ≤0.8 kg	
Environmental	Temperature and Humidity Operating: 0° to +50°C, ≤85% RH (non-condensing) Storage: -30° to +60°C, ≤90% RH (non-condensing)	
CE	EMC	EN61326-1, EN61000-3-2
	LVD	EN61010-1
	RoHS	EN50581

*: When switching to holdover with rubidium oscillator synchronized for at least 30 minutes to GPS or external 1PPS. At this time, the rubidium oscillator must be powered-on, and warmed-up for least 3 hours at a constant ambient temperature.

⚠ CAUTION

Do not subject the equipment mechanical vibration or shock. The accuracy will be degraded if the internal rubidium oscillator is damaged.



High Performance GPS Disciplined Oscillator MU100090A

Ethernet Testing Specifications

Common		
Model No.	MU100010A	MU100011A
Bit Rate	10 Mbps (RJ45), 100 Mbps (RJ45/SFP), 1 Gbps (RJ45/SFP), 10 Gbps (SFP+)	10 Mbps (RJ45), 100 Mbps (RJ45/SFP), 1 Gbps (RJ45/SFP), 10 Gbps (SFP+), 25 Gbps (SFP28), 40 Gbps (QSFP+), 100 Gbps (CFP4/QSFP28)
Test Configuration	RFC 2544, RFC 6349, Y.1564, BERT, Monitor/Generator, Pass-through, Reflector, Channel Stat., Ping, Traceroute, Cable Test, Sync Test	
Encapsulation	Ether Type II (DIX v.2), IEEE 802.3 with 802.2 (LLC1), IEEE 802.3 with SNAP	
100 GbE, 25 GbE FEC	—	FEC enable : On/Off FEC Code : RS (528, 514, 7, 10) FEC status & counter Loss of FEC alignment, Corrected CW, Uncorrectable CW, Corrected Symbol each lane FEC related Error Injection Bit error per CAUI4 lane or 25G CAUI encoded RS-FEC (Injection timing is Single/Burst.)

Configuration, Monitor/Generate		
Model No.	MU100010A	MU100011A
Traffic Generation	<ul style="list-style-type: none"> • Transmitting mode: Normal, Burst • Variable line rate traffic generation, up to full line rate • Line load profile: Constant, Ramp • Traffic duration: Continuous, Programmable number of seconds or frames • Adjustable frame size: 44 bytes to 16000 bytes • Frame sizes: Constant, Stepped, Random • Payload profiles: Data, Video, Voice • User-defined traffic mix of unicast and broadcast frames • Fixed or incremented IP identifier • User programmable DSCP/TOS byte • Configurable IP and Ethernet source and destination addresses (supports IPv4 and IPv6 addressing) <ul style="list-style-type: none"> IPv4: Fixed, DHCP, DNS IPv6: Fixed, NDP • Address increment, Decrement and Random generation supported • User programmable UDP/TCP address • Automatic TCP connect (user selectable) • UDP check sum: Automatic, Fixed (null); TCP check sum: Automatic • Generate pause frames, Respond to pause frames • Answer incoming ARP, Ping requests (On/Off) 	<ul style="list-style-type: none"> • Transmitting mode: Normal, Burst • Variable line rate traffic generation, up to full line rate • Line load profile: Constant, Ramp • Traffic duration: Continuous, Programmable number of seconds or frames • Adjustable frame size: 60 bytes to 16000 bytes • Frame sizes: Constant, Stepped, Random • Payload profiles: Data, Video, Voice • User-defined traffic mix of unicast and broadcast frames • Fixed or incremented IP identifier • User programmable DSCP/TOS byte • Configurable IP and Ethernet source and destination addresses (supports IPv4 and IPv6 addressing) <ul style="list-style-type: none"> IPv4: Fixed, DHCP, DNS IPv6: Fixed, NDP • Address increment, Decrement and Random generation supported • User programmable UDP/TCP address • Automatic TCP connect (user selectable) • UDP check sum: Automatic, Fixed (null); TCP check sum: Automatic • Generate pause frames, Respond to pause frames • Answer incoming ARP, Ping requests (On/Off)
Stacked VLAN	Up to 8 user-settable VLAN tags Parameters per VLAN tag: <ul style="list-style-type: none"> • Ether-type 0x8100 (802.1Q), 0x88a8 (802.1ad), 0x9100 or 0x9200 • User-defined VLAN ID, CFI, VLAN priority • Address increment, Decrement and Random generation supported Only one VLAN level supported at traceroute and RFC 2544 router latency tests. Only two VLAN level supported at ping.	
Multistream	Number of streams: Up to 16 streams per port can be activated	
Timing Functionality	Timing sources (selectable): Internal, Received clock, 2-MHz signal, SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps), PTP (IEEE 1588 v2) recovered clock or signal from optional GPS receiver Frequency deviation: ±100 ppm (1-ppm steps)	Timing sources (selectable): Internal, Received clock, 2-MHz signal, SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps), PTP (IEEE 1588 v2) recovered clock or signal from optional GPS receiver Frequency deviation: ±200 ppm (0.1-ppm steps)
Receive Setting	<ul style="list-style-type: none"> • User-defined expected preamble length: 3 bytes to 15 bytes • User-defined IFG lower threshold: 8 bytes to 15 bytes (RJ45 port only) • User-defined Jumbo frame size upper limit: 1519 bytes to 16000 bytes 	
Error Generation	<ul style="list-style-type: none"> • LAN IFG for Ethernet (RJ45 port only), FCS, Preamble, Error symbol/block, Wrong IP check sum, Fragmented IP, Wrong layer 4 check sum, PRBS bit error, BER test sequence error • PCS (10 Gbps) Invalid block type (0x00, 0x2d, 0x33, 0x66), Invalid sync header (00, 11), Invalid alignment marker, BIP error 	<ul style="list-style-type: none"> • LAN IFG for Ethernet (RJ45 port only), FCS, Preamble, Error symbol/block, Wrong IP check sum, Fragmented IP, Wrong layer 4 check sum, PRBS bit error, BER test sequence error • PCS (10 Gbps, 40 Gbps, 100 Gbps) Invalid block type (0x00, 0x2d, 0x33, 0x66), Invalid sync header (00, 11), Invalid alignment marker, BIP error

Ethernet Testing Specifications

Alarm Generation	No link, Remote fault, Local fault (10 Gbps) PCS (10 Gbps): High BER	No link, Remote fault, Local fault (10 Gbps, 40 Gbps, 100 Gbps) PCS (10 Gbps, 40 Gbps, 100 Gbps) : High BER
PCS Skew	—	40 Gbps, 100 Gbps • Insertion 100 Gbps Tx lane: 0 to 4224 bits 40 Gbps physical lane: 0 to 8448 bits • Detection Relative skew, Marker map

Result, Monitor/Generate		
Model No.	MU100010A	MU100011A
Status	<ul style="list-style-type: none"> Link status, Interface type, Jabber detected, Frames present, MPLS/EoMPLS/VLAN, Speed, Full or half duplex, Local clock (Ethernet 1000 Mbps), LFS LF/RF (Ethernet 10 Gbps), Signal present, Bit rate of incoming Ethernet signal, Auto negotiation complete Link partner abilities: <ul style="list-style-type: none"> Pause capable and Asymmetric pause request (not Ethernet 10 Gbps and more), Remote fault, Speed/Duplex Indicators for Utilization, Throughput and Errored frames Signal level indication for optical Ethernet interfaces LFS local fault, LFS remote fault, High BER, LOA (10 Gbps) Invalid sync header, Invalid alignment marker, BIP error, Invalid block (10 Gbps) 	<ul style="list-style-type: none"> Link status, Interface type, Jabber detected, Frames present, MPLS/EoMPLS/VLAN, Speed, Full or half duplex, Local clock (Ethernet 1000 Mbps), LFS LF/RF (Ethernet 10 Gbps), Signal present, Bit rate of incoming Ethernet signal, Auto negotiation complete Link partner abilities: <ul style="list-style-type: none"> Pause capable and Asymmetric pause request (not Ethernet 10 Gbps and more), Remote fault, Speed/Duplex Indicators for Utilization, Throughput and Errored frames Signal level indication for optical Ethernet interfaces LFS local fault, LFS remote fault, High BER, LOA (10 Gbps, 40 Gbps, 100 Gbps) Invalid sync header, Invalid alignment marker, BIP error, Invalid block (10 Gbps, 40 Gbps, 100 Gbps)
Resolution	User-defined resolution for statistical measurements: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.	
Performance Statistics	• Utilization (Max./Min./Avg.), Throughput (Max./Min./Avg.), Frame rate (Max./Min./Avg.)	
Frame Statistics	<ul style="list-style-type: none"> Total frames, Total valid frames, Unicast/Multicast/Broadcast frames, Number of pause frames Number of VLAN tagged frames, Max. number of VLAN layers detected, Last received VLAN ID, Last received VLAN priority Number of MPLS frames and MPLS-TP frames. Max. number of MPLS layers detected. Last received MPLS Label, MPLS Priority and MPLS TTL. Number of PBB frames. Last received B/I-tag ID and B/I-tag priority. Total errored frames, Fragmented frames, Number of oversized and undersized (runts) frames, Number of FCS errored frames, Error symbol frames (not Ethernet 10 Gbps)/Code violation frames (Ethernet 10 Gbps), Number of collisions (10 Mbps, 100 Mbps half duplex), Preamble violations, IFG violations (RJ45 port only), False carrier, 10G LFS LF (local fault), 10G LFS RF (remote fault) 	
Burst Statistics	Total frames, Total valid frames, Number of burst, Total frames in bursts, Burst size (Max./Min./Avg.)	
Frame Distribution Statistics	<ul style="list-style-type: none"> Total valid/ frames, 64 to 127, 128 to 255, 256 to 511, 512 to 1023, 1024 to 1518 byte frames, Total number of jumbo frames Frame size (Max./Min./Avg.) 	
Multistream Statistics	Available information per stream: <ul style="list-style-type: none"> Frame loss count/rate, Throughput, Latency, Packet jitter, Frames and bytes received and transmitted 	
Transmit Statistics	Total frames, Total valid frames, Unicast/Multicast/Broadcast frames, FCS errors, Total errors 64 to 127, 128 to 255, 256 to 511, 512 to 1023, 1024 to 1518 byte frames, Total number of jumbo frames Total number of frames (Tx (own port) – Rx (selectable port))	
Filter	Up to 8 filter conditions can be defined. Each condition can be applied as filter: IP or MAC source address, IP or MAC destination address, Broadcast address, IEEE OUI value, Encapsulation type, VLAN ID and VLAN tag priority, MPLS, PBB source and destination MAC address, PBB B/I-tag, MPLS-TP source and destination MAC address, TPC/UDP source and destination port, User-defined pattern at defined offset	
PCS Statistics	—	LOBL, Invalid sync header, Invalid Block, High BER, LFS Local Fault, LFS Remote Fault, LOA, Invalid alignment marker, BIP error, LOAML, Marker Map, Relative skew
Adjustable Threshold	Utilization, Throughput, Errored frames, Collision rate, Unicast frames, Multicast frames, Broadcast frames, Pause frames, Fragmented frames, Undersized frames (runts), Oversized frames, FCS errored frames, IFG violations (RJ45 port only), Preamble violations, BER test pattern errors, Sequence errors, Diff.Tx-Rx	
DHCP	<ul style="list-style-type: none"> Display source IPv4 address assigned by DHCP Display current lease expire time Display IPv4 addresses of primary and secondary DNS server when obtained by DHCP Gateway setup using DHCP 	
NDP	• Apply and show source IPv6 address assigned by NDP	

Ethernet Testing Specifications

BER Test and Service Disruption Measurement	
BER Test	<p>Generation and detection of test patterns, Count of errors in received test pattern, Pattern generation: Unframed (Layer 1), Framed Ethernet (MAC) header (Layer 2), Framed Ethernet (MAC) header with IP header (Layer 3) or Framed Ethernet (MAC) header, Framed with IP header and TCP/UDP header (Layer 4), User-defined header pattern (14 byte to 256 byte), Detection of sequence errors, loss of sequence synchronization, Frame loss count and frame loss seconds</p> <p>Throughput measurement results are calculated for:</p> <ul style="list-style-type: none"> Utilization layer, Physical layer, Physical layer excluding preamble, Link layer, Network layer and Data layer Min./Max./Avg. values <p>Performance (M.2100 type) parameters: ES, SES, ALS, UAT, AVT, EFS Test patterns: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, HF test pattern, CRPAT, JTPAT, SPAT, 55 Hex, Fox, 32-bit user programmable User-defined resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.</p>
Error Generation	FCS, Preamble, Error symbol, IFG for Ethernet (RJ45 port only), Wrong IP check sum, Fragmented IP, Wrong layer 4 check sum, PRBS bit error, BER test sequence error
Alarm Generation	No link, Remote fault
Service Disruption Measurement	<p>Service disruption measurement activated as part of BER test</p> <ul style="list-style-type: none"> Max./Avg. service disruption time, Resolution: 0.1 μs Number of service disruptions Disruption Type: Packet, LOS

RFC 2544 Testing	
Test modes:	<ul style="list-style-type: none"> Throughput, Frame loss, Latency or Packet jitter, Back-to-back frames (burstability) <p>End-to-end network test mode (two MU100010A/MU100011A units in Local-remote setup)</p> <ul style="list-style-type: none"> Throughput, Frame loss, Back-to-back frames (burstability) <p>Router latency test mode: IP ping based latency test or packet jitter</p>

Service Activation Test (Y.1564)	
Functions	<ul style="list-style-type: none"> Up to 8 services per port Color-aware and non-color-aware in combinations (IP DSCP or VLAN PCP) Test modes: One-way (uni- or bi-directional, symmetrical or asymmetrical), Round-trip Verification against service acceptance criteria: Information rate, Frame transfer delay, Frame delay variation, Frame loss rate, Availability <p>Optional GPS timing synchronization</p>
Service Configuration Test	<ul style="list-style-type: none"> Subtests for: Committed information rate, Excess information rate, Traffic policing, Committed burst size, Excess burst size Step duration: 1 s to 60 s (user programmable) Number of steps: 1 to 10 (user programmable) Slope: Rising/Falling Results: Pass/Fail indication, IR (Min./Avg./Max.), FL (Count/FLR), FTD, FDV (Min./Avg./Max./Current (during measurement))
Service Performance Test	<ul style="list-style-type: none"> All services tested simultaneously at CIR Duration 15 min, 2 h, 24 h, user programmable Results: Pass/Fail indication, IR (Min./Avg./Max.), FL (Count/FLR), FTD, FDV (Min./Avg./Max./Current (during measurement)), AVAIL (%), Unavail (s)

RFC 6349 TCP Throughput Test	
Supported Bit Rate	10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps
Function	<p>Test Direction Setup</p> <ul style="list-style-type: none"> Local \rightarrow Remote Remote \rightarrow Local Simultaneous in both directions <p>For RFC 6349 test sequence, user can choose to measure for:</p> <ul style="list-style-type: none"> Path MTU Baseline RTT Window Scan and Throughput Multi-Service <p>Multi-Service: DSCP or TOS can be set to each TCP connections</p> <p>Measurement results include:</p> <ul style="list-style-type: none"> Auto-calculation of Bandwidth Delay Product (BDP) Transmitted and Retransmitted Bytes TCP Transfer Time Ratio TCP Efficiency Retransmitted Percentage Buffer Delay Percentage <p>Supports connecting to iPerf server</p>

Ethernet Testing Specifications

Synchronous Ethernet Test	
Supported Bit Rate	10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps
SyncE (ITU-T G.826x) Functionality	<ul style="list-style-type: none"> Specify Quality Level (QL) of transmitted Ethernet signal. Analysis of QL indicated in received Ethernet signal with alarm at missing QL indications. SyncE results: SSM Rx count and rate, SSM Tx count, Indicated QL statistics, SSF seconds ESMC messages captured and exported in Wireshark format.
IEEE 1588 v2 Functionality	<ul style="list-style-type: none"> Each port of the Ethernet interface can act as a timing master or a timing slave independently. Supported profiles: G.8265.1, G.8275.1, User Defined When acting as master in Unicast (G.8265.1) mode, one slave is accepted at a time. If the slave requires 32, 64, or 128 Sync messages per second, IEEE 1588-2008 paragraph 7.7.2.1 specifying 90% confidence interval is not followed. Configurable parameters <ul style="list-style-type: none"> Domain: 0 to 255 Step Mode: One-step, Two-step Delay Mechanism: Delay request/response, Peer delay Negotiation: On/Off (Unicast only) Clock Source: Internal or UTC locked with GPS Clock Identify Priority#1, #2, Class, Time Source, Accuracy Index: 0 to 255 Announce Interval: 1/8 to 32 s Announce Timeout: 2 to 255 s Sync Interval: 1/128 to 32 s Minimum Delay Request Interval: 1/128 to 32 s Unicast Duration: 60 s to 1,000 s Protocol Stack <ul style="list-style-type: none"> Layer2: Ethernet, Ethernet/VLAN, Ethernet/MPLS Layer3: None, IPv4, IPv6 PTP Protocol Analysis <ul style="list-style-type: none"> Statistics of IEEE 1588 messages and message rate. Logged IEEE 1588 events: Clock state transitions, State transition events, Faults and Changes in grand-master clock IEEE 1588 messages captured and exported in Wireshark format
Sync Test Application (MT1000A-005 and MU100090A are required)	<p>Time/Phase Synchronization Test</p> <p>Supported Interface: GigE, 10 GigE (only optical interface)</p> <ul style="list-style-type: none"> Two methods of TE (Time Error) testing, which are able to run simultaneously 1 PPS TE Method: Comparing 1 PPS signal from the network with reference 1 PPS. (Accuracy is 5 ns) Test Items: max TE , cTE, dTE Packet TE Method: Comparing time stamps inside PTP message with GPS locked UTC. Test Items: max TE1 , max TE4 , cTE1, cTE4

IP Channel Statistics	
Supported Bit Rate	10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps
Statistics	<p>Statistics for up to 230 channels, identified by user-defined combinations of:</p> <ul style="list-style-type: none"> IPv4, IPv6 or MAC address VLAN ID or MPLS label Protocol information IP next header (protocol) TCP/UDP ports <p>Traffic capacity:</p> <ul style="list-style-type: none"> 10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps, line speeds: 100% line load <p>Available Information per channel:</p> <ul style="list-style-type: none"> Frame count/rate, Throughput, Byte count, MPLS frames, IP frame/packet size distribution, IP header bytes, IP fragments, TTL threshold violations, IP packet count/rate, IP bytes, IP throughput, IP header errors, TCP/UDP bytes, TCP/UDP packet count/rate, Throughput, TCP/UDP errored packets, Undersize frames, Oversize frames

Ethernet Testing Specifications

Ethernet Frame Capture		
Model No.	MU100010A	MU100011A
Capture Buffer Size	1 Mbytes to 128 Mbytes (10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps) When capture buffer full: Stop or Wrap	1 Mbytes to 128 Mbytes (10 Mbps, 100 Mbps, 1 Gbps, 10 Gbps) 512 kbytes (25 Gbps, 40 Gbps, 100 Gbps) When capture buffer full: Stop or Wrap
Capture Frame Slicing	If activated capture frame is first 64 bytes or 128 bytes of each frame (ignores rest of the frame)	
Timestamp Resolution	10 ns	
Include Tx Frame	On/Off	
Capture Trigger	Manual, On error, Field match Trigger position: Top, Middle	
Trigger Error	Fragmented frames, Oversize frames, Undersized frames, Undersized and oversized frames, FCS errored frames, Any type	
Trigger Field Pattern Data	Enabled when capture trigger setting is "Field match" • Offset: 0 to 15999 bytes • Length: 1 bytes to 16 bytes	
Capture Data	Pcap format for display in Wireshark	

Wireshark® is a registered trademark of the Wireshark Foundation.

Discovery	
Function	The Auto Discovery feature allows a local Network Master Pro user to discover other remote Network Master Pro units on the network to complete testing. The local user can manage multiple testers remotely without a dedicated network connection. Support IP version: IPv4
Setting	Network: Src MAC Address, Src IP Address, Network Mask, Gateway, DHCP Connection Security: Password VLAN: OFF, 1, 2 Frame Capture: On/Off

MPLS/MPLS -TP	
Number of MPLS Header	Up to 8 MPLS headers set by user
Parameters per MPLS Header	User-defined label, Exp and TTL fields in each MPLS header • Address increment, Decrement and Random generation An EoMPLS (Ethernet over MPLS) or PWE3 (Pseudo-wire emulation edge-to-edge) label (RFC 4448 control word) can be added. MPLS can only transport VLAN if EoMPLS activated.
Statistics	• Number of labels (Max./Min.) • Number of MPLS-TP frames • Last received MPLS-TP label/priority/TTL
OAM (MPLS-TP)	ITU-T G.8113.1 comply Supported OAM messages • ITU-T Y.1731: CCM, LBM, LBR, LTM, LTR, AIS, LCK, TST, MCC, LMM, LMR, 1DM, DMM, DMR, EXM, EXR, VSM, VSR, SLM, SLR • IEEE 802.1ag: CCM, LBM, LBR, LTM, LTR

PBB (Mac-in-Mac MiM)	
Programmable Field	B-tag, I-tag, MAC destination and source addresses
Result	Number of PBB frames, Last received B-tag VLAN ID, Last received B-tag priority, Last received I-tag priority, Last received I-tag service ID
OAM	Supported OAM messages • ITU-T Y.1731: CCM, LBM, LBR, LTM, LTR, AIS, LCK, TST, MCC, LMM, LMR, 1DM, DMM, DMR, EXM, EXR, VSM, VSR, SLM, SLR • IEEE 802.1ag: CCM, LBM, LBR, LTM, LTR

Ethernet Testing Specifications

Ethernet OAM	
OAM Standards Supported	<ul style="list-style-type: none"> ITU-T Y.1731 (Service layer OAM) IEEE 802.1ag (Connectivity layer OAM) IEEE 802.3 (formerly IEEE 802.3ah) (Access link OAM)
Messages Supported	Generates and receives following OAM messages. Supported OAM messages <ul style="list-style-type: none"> ITU-T Y.1731: CCM, LBM, LBR, LTM, LTR, AIS, LCK, TST, MCC, LMM, LMR, 1DM, DMM, DMR, EXM, EXR, VSM, VSR, SLM, SLR IEEE 802.1ag: CCM, LBM, LBR, LTM, LTR IEEE 802.3ah: Information, Variable request, Variable response, Loopback control
IEEE 802.3ah Function	<ul style="list-style-type: none"> Discovery Loopback activate
Statistics	<ul style="list-style-type: none"> Number of each message generated/received

Ping Test	
Function	For Connectivity and Configuration check <ul style="list-style-type: none"> Round Trip Time (RTT) Supports IPv4 and IPv6 addressing Answer incoming ping requests (On/Off)

Reflector	
Function	The following parameters are user selectable: <ul style="list-style-type: none"> Reflector MAC/IP address Swap all MAC addresses or one specific MAC address Swap IP addresses Swap port numbers on UDP/TCP frames Force ACK on TCP frames Answer incoming ARP, Ping requests

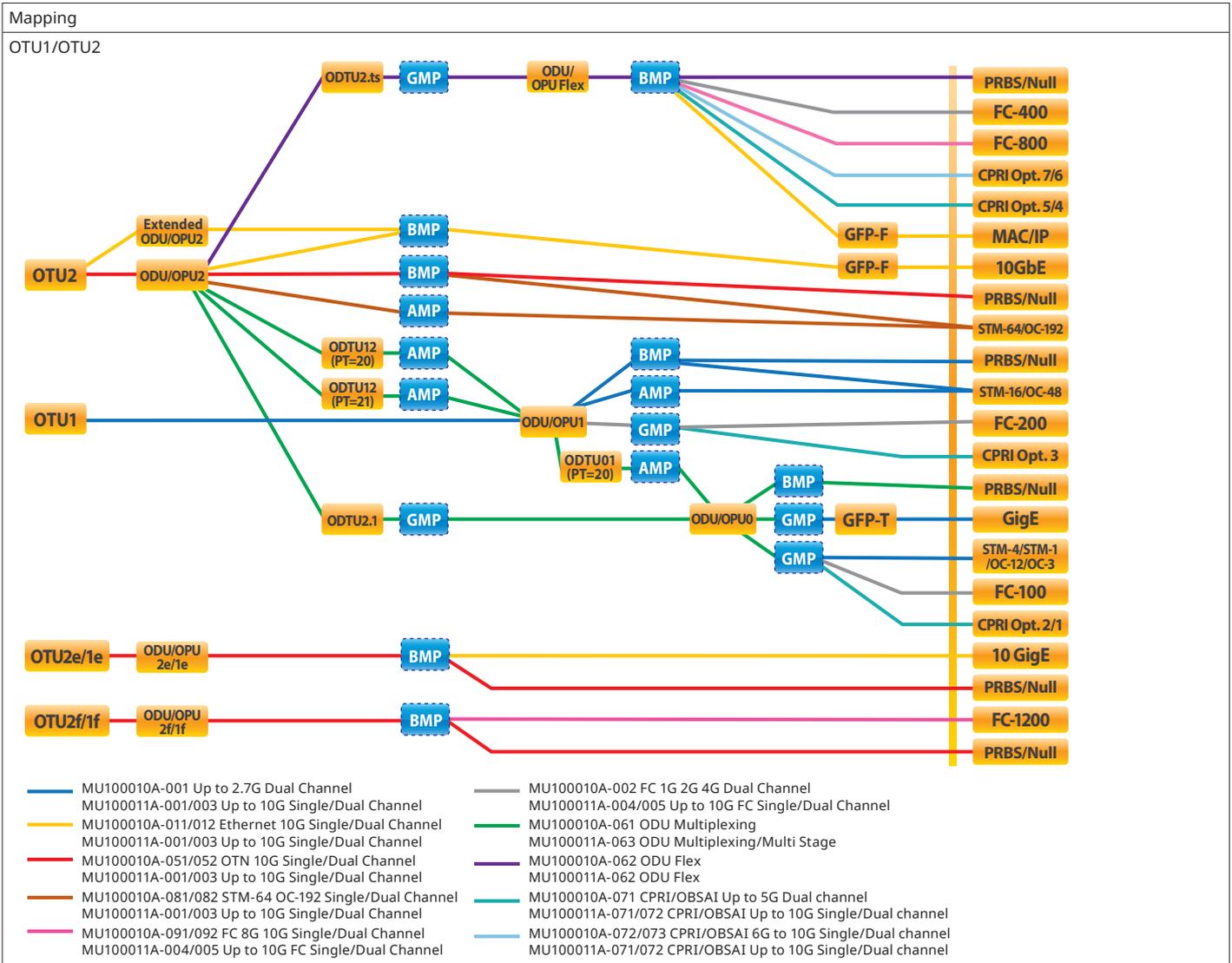
Traceroute	
Function	Trace IP route over IP network <ul style="list-style-type: none"> User-defined Max. number of hops (1 to 255) Information per hop: Ping time (Min./Max./Avg.), Number of ping timeouts

Cable Test	
Function	Identifies cable faults like short circuits, or breaks in wire pair, and displays distance from instrument to fault Support Cable: category 5/5e, 6/6a

10G WAN-PHY	
WAN Mode	10 Gbps Ethernet (normal)
Terminology	SDH or SONET
Error Generation	SDH: A1A2, B1, B2, MS-REI, B3, HP-REI SONET: A1A2, B1, B2, REI-L, B3, REI-P
Alarm Generation	SDH: LOF, OOF, MS-AIS, MS-RDI, MS-TIM, AU-AIS, AU-LOP, HP-PLM, HP-UNEQ, HP-TIM, HP-RDI, LCD SONET: LOF, SEF, TIM-S, AIS-L, RDI-L, AIS-P, LOP-P, TIM-P, PLM-P, UNEQ-P, RDI-P
Error Measurement	SDH: A1A2, B1, B2, MS-REI, B3, HP-REI SONET: A1A2, B1, B2, REI-L, B3, REI-P G.826, G.828+G.829 or M.2101.1 (M.2100) error performance parameters are calculated
Alarm Detection	SDH: LOS, LOF, OOF, MS-AIS, MS-RDI, MS-TIM, AU-AIS, AU-LOP, HP-PLM, HP-UNEQ, HP-TIM, HP-RDI, LCD, LSS SONET: LOS, LOF, SEF, TIM-S, AIS-L, RDI-L, AIS-P, LOP-P, TIM-P, PLM-P, UNEQ-P, RDI-P, LCD-P, LSS
Overhead Byte Functionality	Generation of user-defined overhead bytes Capture and display of current overhead bytes

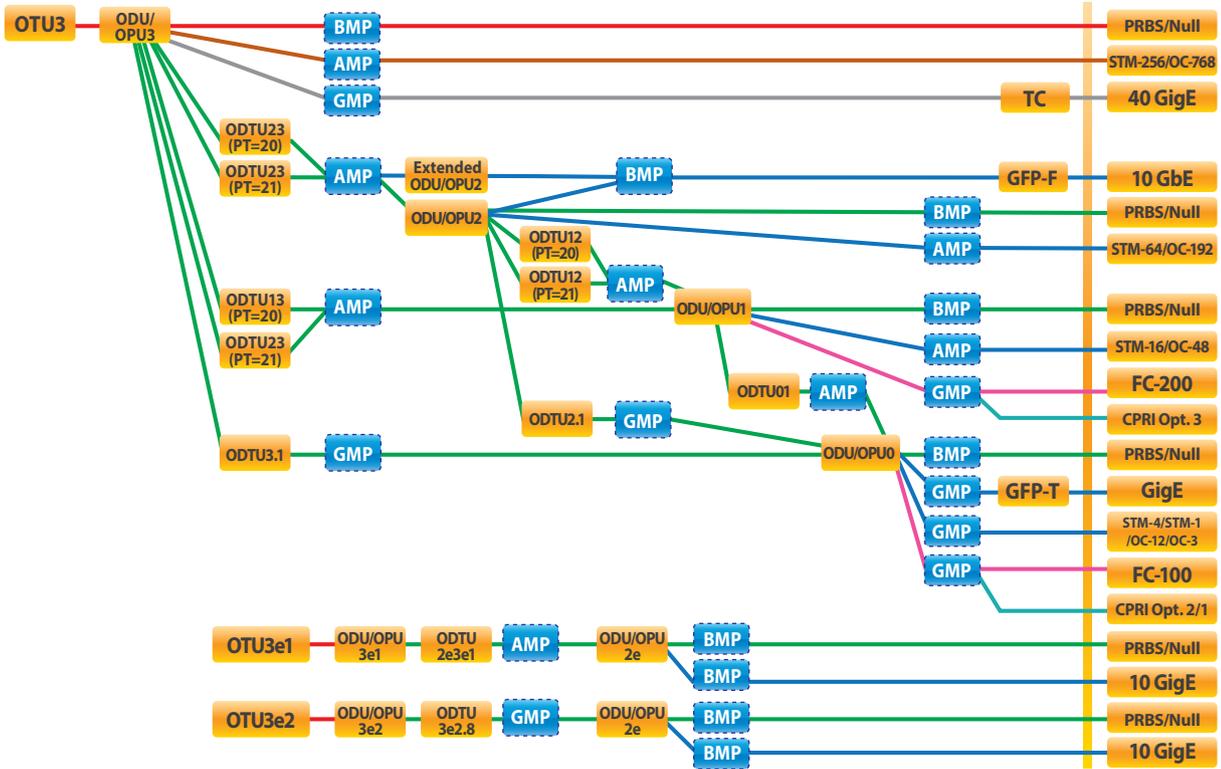
OTN Testing Specifications

Common		
Model No.	MU100010A	MU100011A
Framing	OTU2, OTU2e, OTU1e, OTU2f, OTU1f, OTU1	OTU4, OTU3, OTU3e1, OTU3e2, OTU2, OTU2e, OTU1e, OTU2f, OTU1f, OTU1
Test Configuration	BERT, RTD, APS	
Transmitter Clock	<ul style="list-style-type: none"> Internal clock accuracy: ± 4.6 ppm, Clock offset: ± 50 ppm (1-ppm steps) Received clock TTL level external 2 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps) Signal from optional GPS receiver 	<ul style="list-style-type: none"> Internal clock accuracy: ± 4.6 ppm, Clock offset: ± 200 ppm (0.1-ppm steps) Received clock TTL level external 2 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps) Signal from optional GPS receiver
Receive Signal Rate	± 50 ppm Frequency deviation indication resolution: ± 1 ppm	± 200 ppm Frequency deviation indication resolution: ± 0.1 ppm
Scrambling	Complies with ITU-T G.709	



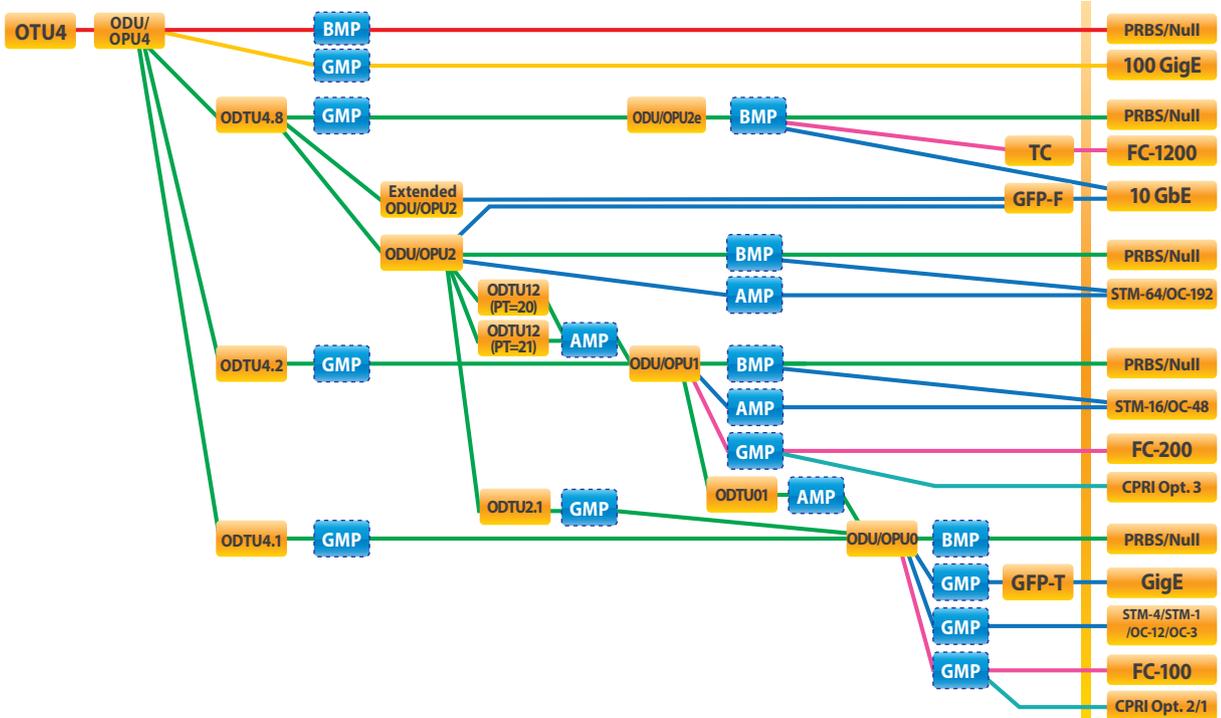
OTN Testing Specifications

OTU3



- MU100011A-053 OTN 40G Single Channel
- MU100011A-063 ODU Multiplexing/Multi Stage
- MU100011A-001/003 Up to 10G Single/Dual Channel
- MU100011A-004/005 Up to 10G FC Single/Dual channel
- MU100011A-071/072 CPRI/OBSAI Up to 10G Single/Dual Channel
- MU100011A-013 Ethernet 40G Single Channel
- MU100011A-083 STM-256/OC-768 Client Signal

OTU4



- MU100011A-055 OTN 100G Single Channel
- MU100011A-063 ODU Multiplexing/Multi Stage
- MU100011A-001/003 Up to 10G Single/Dual Channel
- MU100011A-004/005 Up to 10G FC Single/Dual Channel
- MU100011A-071/072 CPRI/OBSAI Up to 10G Single/Dual Channel
- MU100011A-015 Ethernet 100G Single Channel

OTN Testing Specifications

OTN Configuration		
Model No.	MU100010A	MU100011A
OTN Alarms	<p>Detected alarms</p> <ul style="list-style-type: none"> • OTU layer: OTU-AIS, LOF, OOF, LOM, OOM, SM-TIM, SM-BIAE, SM-BDI, SM-IAE • ODU layer: LOS, ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSF, BSD • ODU multiplexing: ODU-LOFLOM, ODU-OOF, OOM, ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, MSIM • OPU layer: PLM, OPU-MSIM, Client-AIS, CSF, LSS • TCM: TCMi-TIM, TCMi-BIAE, TCMi-BDI, TCMi-IAE, TCMi-LTC (i = 1 to 6) <p>Generated alarms</p> <ul style="list-style-type: none"> • OTU layer: OTU-AIS, OTU-OOF/LOF, OOM/LOM, SM-TIM, SM-BIAE, SM-BDI, SM-IAE • ODU layer: ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSF, BSD • ODU multiplexing: OOF/LOF, OOM/LOM, ODU-AIS, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSF, BSD • OPU layer: Client-AIS, CSF • TCM: TCMi-TIM, TCMi-BIAE, TCMi-IAE, TCMi-BDI, TCMi-LTC (i = 1 to 6) 	<p>Detected alarms</p> <ul style="list-style-type: none"> • OTU layer: OTU-AIS, LOF, OOF, LOM, OOM, SM-TIM, SM-BIAE, SM-BDI, SM-IAE • ODU layer: LOS, ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSF, BSD • ODU multiplexing: ODU-LOFLOM, ODU-OOF, OOM, ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, MSIM • OPU layer: PLM, OPU-MSIM, Client-AIS, CSF, LSS • TCM: TCMi-TIM, TCMi-BIAE, TCMi-BDI, TCMi-IAE, TCMi-LTC (i = 1 to 6) <p>Generated alarms</p> <ul style="list-style-type: none"> • OTL: LOF, OOF, OOR, LOR, ILA/OLA (OTU4, OTU3, OTU3e1, OTU3e2) <p>Generated alarms</p> <ul style="list-style-type: none"> • OTU layer: OTU-AIS, OTU-OOF/LOF, OOM/LOM, SM-TIM, SM-BIAE, SM-BDI, SM-IAE • ODU layer: ODU-AIS, ODU-OCI, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSF, BSD • ODU multiplexing: OOF/LOF, OOM/LOM, ODU-AIS, ODU-LCK, PM-TIM, PM-BDI, FSF, FSD, BSF, BSD • OPU layer: Client-AIS, CSF • TCM: TCMi-TIM, TCMi-BIAE, TCMi-IAE, TCMi-BDI, TCMi-LTC (i = 1 to 6) • OTL: LOF, OOF, OOR, LOR (OTU4, OTU3, OTU3e1, OTU3e2)
OTN Errors	<p>Detected errors</p> <ul style="list-style-type: none"> • OTU layer: FAS, MFAS, SM-BEI, SM-BIP8, FEC-Correctable, FEC-Uncorrectable • ODU layer: PM-BIP8, PM-BEI • OPU layer: Pattern error • GMP error: CRC8 error, CRC5 error • GFP errors: <ul style="list-style-type: none"> • cHEC corrected, cHEC uncorrectable, tHEC corrected, tHEC uncorrectable, CSF Signal, CSF Sync, Invalid GFP Frame, Superblock CRC, eHEC corrected, eHEC uncorrectable, FCS, CMF Sync, CMF Signal, SSF, PTI Mismatch, UPI Mismatch • TCM: TCMi-BEI, TCMi-BIP-8 (i = 1 to 6) <p>Generated errors</p> <ul style="list-style-type: none"> • OTU layer: Bit all, FAS, OTU-FAS, MFAS, SM-BIP8, SM-BEI • ODU layer: PM-BIP8, PM-BEI, ODU-FAS • TCMi-BIP8, TCMi-BEI (i = 1 to 6) • Pattern error • GMP: CRC8, CRC5, Invalid JC1, Invalid JC2, Invalid JC1&JC2 • GFP: cHEC, tHEC, Superblock CRC, eHEC, FCS, CMF Inserted Error bits are editable. 	<p>Detected errors</p> <ul style="list-style-type: none"> • OTU layer: FAS, MFAS, SM-BEI, SM-BIP8, FEC-Correctable, FEC-Uncorrectable • ODU layer: PM-BIP8, PM-BEI • OPU layer: Pattern error • GMP error: CRC8 error, CRC5 error • GFP errors: <ul style="list-style-type: none"> • cHEC corrected, cHEC uncorrectable, tHEC corrected, tHEC uncorrectable, CSF Signal, CSF Sync, Invalid GFP Frame, Superblock CRC, eHEC corrected, eHEC uncorrectable, FCS, CMF Sync, CMF Signal, SSF, PTI Mismatch, UPI Mismatch • TCM: TCMi-BEI, TCMi-BIP-8 (i = 1 to 6) • OTL: MFAS, LLM (OTU4, OTU3, OTU3e1, OTU3e2) <p>Generated errors</p> <ul style="list-style-type: none"> • OTU layer: Bit all, FAS, OTU-FAS, MFAS, SM-BIP8, SM-BEI • ODU layer: PM-BIP8, PM-BEI, ODU-FAS • TCMi-BIP8, TCMi-BEI (i = 1 to 6) • Pattern error • GMP: CRC8, CRC5, Invalid JC1, Invalid JC2, Invalid JC1&JC2 • GFP: cHEC, tHEC, Superblock CRC, eHEC, FCS, CMF Inserted Error bits are editable. • OTL: MFAS, LLM (OTU4, OTU3, OTU3e1, OTU3e2)
Error Performance	• G.8201/M.2401 analysis of received signal based on detected errors and alarms: BBE, BBER, SES, SESR, UNAV	
Justification Analysis	<p>Count</p> <ul style="list-style-type: none"> • AMP: Positive (+1), Positive (+2), Negative (-1), Offset (ppm) • GMP: CRC8 Error, CRC5 Error, Inc, Inc > 1, Inc > 2, Inc Over, Dec, Dec > 1, Dec > 2, Dec Over, Offset (ppm), Cm (t) Max., Cm (t) Min. 	
BER Test Pattern	<p>Pattern generation and detection for bulk test patterns:</p> <ul style="list-style-type: none"> • Test patterns: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, Null <p>PRBS patterns can be inverted.</p> <ul style="list-style-type: none"> • User-defined patterns (Pattern length: 2048 bits, 32 bits) 	
FEC Test	ITU-T O.182 Random error insertion	
Overhead	<p>User-editable header bytes</p> <ul style="list-style-type: none"> • OTU layer: FAS, SM, GCC0, RES • ODU layer: PM, FTFL, APS/PCC, GCC1, GCC2, RES, EXP, TCMi (i = 1 to 6) • OPU layer(s): PSI <p>Capture and display current overhead bytes</p> <ul style="list-style-type: none"> • The following signals are decoded: TTI (SM, PM, TCMi (i = 1 to 6) of high-order, FTFL, PT) 	

OTN Testing Specifications

OTL Skew	—	OTU4, OTU3, OTU3e1, OTU3e2 • Insertion Bits: 0 to 32000 (LLD) • Detection Relative skew, Marker map
Client Signal	Ethernet functionality is supported for Ethernet signals embedded in selected ODU-n (Requires MU100010A-061, MU100010A-062 and MU100010A-001/011/012) SDH/SONET functionality is supported for SDH/SONET signals embedded in selected ODU-n (Requires MU100010A-061 and MU100010A-001/081/082) Fibre Channel functionality is supported for Fibre Channel signals embedded in selected ODU-n (Requires MU100010A-061 and MU100010A-002/091/092)	Ethernet functionality is supported for Ethernet signals embedded in selected ODU-n (Requires MU100011A-062, MU100011A-063 and MU100011A-001/003/013/015) SDH/SONET functionality is supported for SDH/SONET signals embedded in selected ODU-n (Requires MU100011A-063 and MU100011A-001/003/083) Fibre Channel functionality is supported for Fibre Channel signals embedded in selected ODU-n (Requires MU100011A-063 and MU100011A-004/005)
Through Mode	<ul style="list-style-type: none"> • Transparent mode • OH overwrite mode <p>The OTU, ODU and OPU overhead can be changed. The FEC encoder and decoder can be set On/Off in any mode</p>	

OTN Results	
Status	Current information on: <ul style="list-style-type: none"> • Alarms and errors on monitored line • Input level indication for optical signals • Frequency • Frequency deviation
Statistics	User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Logged information: Alarms (s), Errors (count or count and ratio), Client Frequency, Deviation
APS	APS (Automatic Protection Switching) test and analysis <ul style="list-style-type: none"> • APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted. • Start and stop triggers can be selected independently. • Trigger events can be selected from the high-order OTU and ODU, Pattern bit error, LOS (Loss of Signal). • Switching time, Switching count, Pass/Fail, Minimum, Maximum and Average can be displayed. • APS switching time measurement resolution: 0.1 ms
Round Trip Delay (Propagation Time) Measurement	Resolution: 0.1 μ s Measured Max. time: 10.0 s Interval: 0.5, 1, 2, 5, 10 s
Tributary Scan	Supports up to 10 Gbps Detected alarms: OTU-AIS, LOF, OOF, LOM, OOM, SM-BIAE, SM-BDI, SM-IAE, ODU-AIS, ODU-OCI, ODU-LCK, PM-BDI, LOFLOM

CPRI/OBSAI Testing Specifications

Common	
Bit Rate	CPRI: 614.4, 1228.8, 2457.6, 3072.0, 4915.2, 6144.0, 9830.4, 10137.6 Mbps OBSAI: 768, 1536, 3072.0, 6144.0 Mbps
Test Configuration	BERT, Pass Through

BER Test		
Model No.	MU100010A	MU100011A
Port Mode	Off, Normal, Through	
Transmitter Clock	Reference Clock <ul style="list-style-type: none"> • Internal clock • External clock • BITS • SETS • 2 MHz • 10 MHz • GPS • Received clock 	
Content	Unframed, CPRI Link	
Pattern	PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, User 32 bits, Off	
CPRI Link	Start up: Enabled, Disabled Role: Master, Slave Protocol version: 1, 2 HDLC rate: no HDLC, 240, 480, 960, 1920, 2400 kbit/s, Highest possible Ethernet: On, Off; Pointer: 20 to 63	
Alarm Insertion	Signal Loss, LOS, LOF, LSS, Remote-LOS, Remote-LOF, RAI, SDI, Reset	
Error Insertion	Item: LCV, SHV, K30.7, Pattern error Insertion timing: Manual, Rate	
Frequency Offset	±100 ppm, 1-ppm steps	±200 ppm, 0.1-ppm steps
Alarm Detection	Signal loss, LOS, LOF, LSS	
Error Detection	LCV, SHV, K30.7, Pattern error	
Remote Status	Remote LOS, Remote LOF, RAI, SDI, Reset	
Link	Rx: Protocol version, HDLC rate, Pointer P Tx: Protocol version, HDLC rate, Pointer P	
Statistics	Alarms: Signal loss, LOS, LOF, LSS, Remote LOS, Remote LOF, RAI, SDI, Reset Errors: LCV, SHV, K30.7, Pattern error Frames count: Rx hyper frame, Rx code words, Tx hyper frame, Tx code words Delay: Delay, Average Delay, Min. Delay, Max. Delay Measurement count	
APS	APS (Automatic Protection Switching) test and analysis <ul style="list-style-type: none"> • APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted. • Trigger events (user selectable) • Alarm: Signal Loss, LOS, LOF • Error: LCV, SHV, Pattern error • Remote Alarm: Remote LOS, Remote LOF, RAI, SDI, Reset • Switching time, Switching count, Pass/Fail, Minimum, Maximum and Average can be displayed. • APS switching time measurement resolution: 1 µs 	

Pass Through	
Port Mode	Off, Normal
Statistics	Alarms: Signal loss, LOS, LOF, LSS, Remote LOS, Remote LOF, RAI, SDI, Reset Errors: LCV, SHV, K30.7, Pattern error

Fibre Channel Testing Specifications

Common		
Model No.	MU100010A	MU100011A
Bit Rate	1.0625 Gbps (FC-100/1GFC), 2.125 Gbps (FC-200/2GFC), 4.25 Gbps (FC-400/4GFC), 8.5 Gbps (FC-800/8GFC), 10.52 Gbps (FC-1200/10GFC)	1.0625 Gbps (FC-100/1GFC), 2.125 Gbps (FC-200/2GFC), 4.25 Gbps (FC-400/4GFC), 8.5 Gbps (FC-800/8GFC), 10.52 Gbps (FC-1200/10GFC), 14.025 Gbps (FC-1600/16GFC)
Test Configuration	FC-Performance Test, FC-BERT, FC-Reflector	

FC-BERT		
Model No.	MU100010A	MU100011A
Port mode	Off, 1GFC, 2GFC, 4GFC, 8GFC, 10GFC	Off, 1GFC, 2GFC, 4GFC, 8GFC, 10GFC, 16GFC
Timing source	Internal, External, GPS, Received	
Topology	Point-to-point, Fabric, E-Port	
Flow Control	Credit based transmitter: On/Off Buffer-to-buffer credit configuration: 1 to 65535, Buffer-to-buffer credit and R_RDY counters, R_RDY injection	
Source	Port WWN: User define or Default ID: 000000 to FFFFFFFF	
Destination	Port WWN: User define or Default ID: 000000 to FFFFFFFF	
Primitive Sequence Protocol	Count and transmit primitive sequence: LR, LRR, NOS, OLS	
Traffic Generation	<ul style="list-style-type: none"> • 1GFC (with SOF and EOF frame delimiters and 2GFC frames), Class-3 service frames • Traffic shaping: Constant, Ramp, Burst, 2GFC frame header configuration • Frame length configuration: 3240 bytes (max.) 	
BER test	<ul style="list-style-type: none"> • Test modes: Unframed BER test, Layer 1 BER test, Layer 2 BER test • Test patterns: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, HF test pattern, CSPAT, CJPAT, CRPAT, JTPAT, SPAT, 55 Hex, Fox, 32-bit user programmable, ZERO • Error injection: Bit, CRC, Symbol • Results: Pattern loss seconds, Traffic loss seconds, Bit error count, BER 	
Measurement	<ul style="list-style-type: none"> • Alarm detection: LOS, Link down, Pattern loss • Service disruption measurement: Average/Max service disruption, Number of service disruptions • Traffic statistics: Bandwidth utilization, Data rate, Frame count, Byte count, Frame size distribution, Buffer-to-buffer credit count, R_RDY count, Frame loss count, Round trip delay, Packet jitter, Bit errors, CRC errors, Symbol errors, LR, LRR, NOS, OLS 	

FC-Reflector		
Model No.	MU100010A	MU100011A
Port mode	Off, 1GFC, 2GFC, 4GFC, 8GFC, 10GFC	Off, 1GFC, 2GFC, 4GFC, 8GFC, 10GFC, 16GFC
Timing source	Internal, External, GPS, Received	
Topology	Point-to-point, Fabric, E-Port	
Flow Control	Credit based transmitter: On/Off Buffer-to-buffer credit configuration: 1 to 65535, Buffer-to-buffer credit and R_RDY counters, R_RDY injection	
Source	Port WWN: User define or Default	
Frame Setup	Framing: SOF:Data:EOF or SOF:Header:Data:CRC:EOF	

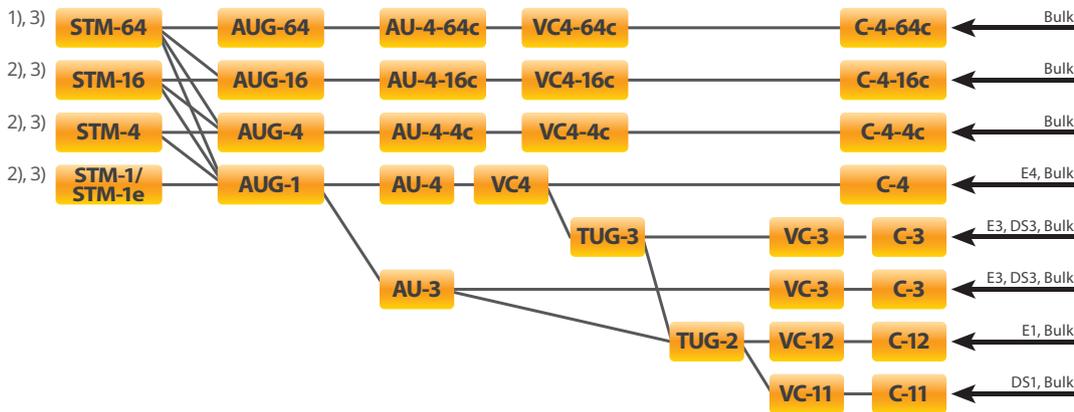
FC-Performance Test		
Model No.	MU100010A	MU100011A
Port mode	Off, 1GFC, 2GFC, 4GFC, 8GFC, 10GFC	Off, 1GFC, 2GFC, 4GFC, 8GFC, 10GFC, 16GFC
Timing source	Internal, External, GPS, Received	
Topology	Point-to-point, Fabric, E-Port	
Flow Control	Credit based transmitter: On/Off Buffer-to-buffer credit configuration: 1 to 65535, Buffer-to-buffer credit and R_RDY counters, R_RDY injection	
Source	Port WWN: User define or Default ID: 000000 to FFFFFFFF	
Destination	Port WWN: User define or Default ID: 000000 to FFFFFFFF	
Configuration	Test Mode: Port-to-Port, Loopback Kinds of test: Throughput, Traffic Profile, Latency, Burst, Credit Frame Size: User Defined (64, 128, 256, 512, 768, 2014, 1280, 2140, up to 3240), Stepped (40 to 3240), Constant	

SDH/SONET Testing Specifications

Common		
Model No.	MU100010A	MU100011A
Bit Rate	Optical (SFP/SFP+): STM-1/OC-3, STM-4/OC12, STM-16/OC48, STM-64/OC192 Electrical (BNC): STM-1e, STS-3	Optical (SFP/SFP+): STM-1/OC-3, STM-4/OC-12, STM-16/OC-48, STM-64/OC-192
Test Configuration	BERT, RTD, APS	

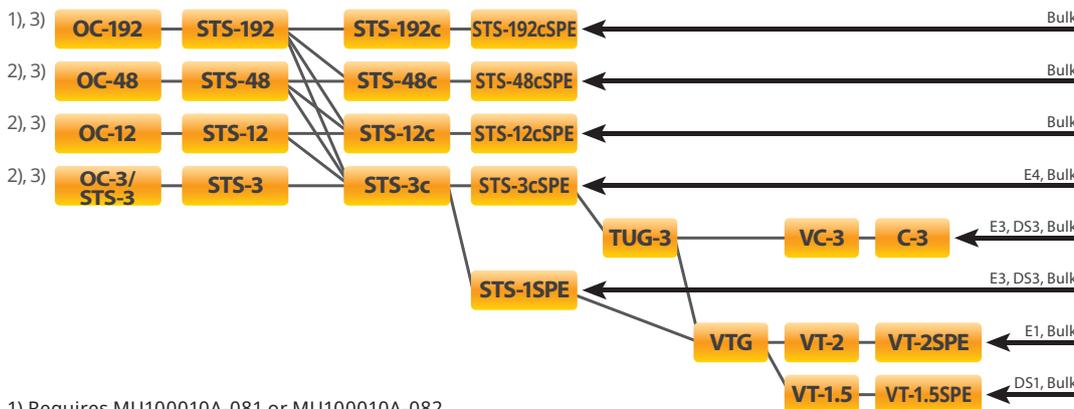
SDH and SONET Configuration		
Model No.	MU100010A	MU100011A
Framing	SDH: Complies with ITU-T G.707, SONET: Complies with Telcordia GR-253	
Transmitter Clock	<ul style="list-style-type: none"> Internal clock accuracy: 4.6 ppm, Clock offset: ± 50 ppm (1-ppm steps) Recovered clock TTL level external 2 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps) 	<ul style="list-style-type: none"> Internal clock accuracy: 4.6 ppm, Clock offset: ± 200 ppm (0.1-ppm steps) Recovered clock TTL level external 2 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
Receive Signal Rate	± 50 ppm Frequency deviation indication resolution: ± 1 ppm	± 200 ppm Frequency deviation indication resolution: ± 0.1 ppm
TCM Frame Format	ITU-T G.783, G.707 Annex D (TCM option 2) and Annex E, POH bytes: N1 (VC-4, VC-3), Z5 (STS-3c, STS-1), N2 (VC-12, VC-11), Z6 (VT-2, VT-1.5) TCM Access Point Identifier (Apid): 15 bytes ASCII sequence, CRC-7	
Scrambling	SDH: Complies with ITU-T G.707, SONET: Complies with Telcordia GR-253	

SDH Mapping



- 1) Requires MU100010A-081 or MU100010A-082
- 2) Requires MU100010A-001
- 3) Requires MU100011A-001 or MU100011A-003

SONET Mapping



- 1) Requires MU100010A-081 or MU100010A-082
- 2) Requires MU100010A-001
- 3) Requires MU100011A-001 or MU100011A-003

SDH/SONET Testing Specifications

Alarms	<p>Detected and generated alarms</p> <ul style="list-style-type: none"> SDH: LOS, LOF, OOF, MS-AIS, MS-RDI, AU-AIS, AU-LOP, HP-PLM, HP-UNEQ, HP-TIM, HP-RDI, TU-LOM, TU-AIS, TU-LOP, LP-PLM, LP-UNEQ, LP-TIM, LP-RDI, LSS SONET: LOS, LOF, OOF, AIS-L, RDI-L, AIS-P, LOP-P, TIM-P, PLM-P, UNEQ-P, RDI-P, LOM-V, AIS-V, LOP-V, PLM-V, UNEQ-V, RDI-V, TIM-V, LSS TCM: TC-LTC, TC-TIM, TC-UNEQ, TC-AIS, TC-RDI, TC-ODI <p>Inserted alarms</p> <ul style="list-style-type: none"> Permanent Alternate: 1 to 8000 consecutive alarm frames, 1 to 8000 consecutive normal frames
Errors	<p>Detected and generated errors</p> <ul style="list-style-type: none"> SDH: A1/A2, B1, B2, MS-REI, B3, HP-REI, V5/B3, LP-REI, Pattern error, ERR trans SONET: A1/A2, B1, B2, REI-L, B3, REI-P, V5/B3, REI-V, Pattern error, ERR trans TCM: TC-IEC, TC-BIP2, TC-REI, TC-OEI <p>Error insertion</p> <ul style="list-style-type: none"> Manual: 1 to 8000 consecutive errors (excluding Pattern error) 1 to 4000 consecutive errors (for Pattern error) Continuous: 10^{-3}, 10^{-4}, 10^{-5}, 10^{-6}, 10^{-7}, 10^{-8}, 10^{-9}, 10^{-10} (The available highest rate varies depending on the error item.) Alternate: 1 to 8000 consecutive error frames, 1 to 8000 consecutive normal frames (excluding Pattern error and ERR trans) 1 to 4000 consecutive error bits, 100 to 4000 consecutive normal bits (for Pattern error)
BER Test Pattern	<p>Pattern generation and detection for O.181 bulk test pattern</p> <ul style="list-style-type: none"> Test patterns supported: PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31 <p>PRBS patterns can be inverted.</p> <p>All 0 s, All 1 s, Alternating 1:1, Alternating 1:3, Alternating 1:7, 2 in 8</p> <p>User-defined patterns (Pattern length: up to 2048, Length step: 8-bit)</p>
Pointer	<ul style="list-style-type: none"> Support pointer events monitoring and generation Pointer test sequences: None, Single alternating, Regular + Double, Regular + Missing, Double alternating Display pointer value of receiver side Graphical display of pointer movements
Overhead	<ul style="list-style-type: none"> Generation of section/transport and path overhead bytes Display of current section/transport and path overhead bytes <p>All overhead can be decoded, including decoded J0, J1, J2 byte.</p>
Through Mode	<ul style="list-style-type: none"> Transparent mode OH overwrite mode: Can be changed SOH (SDH), TOH (SONET)

SDH and SONET Results	
Status	<p>Current information on</p> <ul style="list-style-type: none"> Alarms and errors on monitored line Input level indication for optical signals Input level indication for electrical signals Actual bit rate Frequency deviation
Statistics	<p>User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h</p> <p>Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Pointer operations</p> <p>Event log: Major measurement events incl. errors, alarms and pointer operations are logged with 1-second resolution.</p>
Error Performance	G.826/G.828/G.829/M.2100 analysis of received signal based on detected errors and alarms: ES, SES, BBE (not M.2100), UNAV
APS	<p>APS (Automatic Protection Switching) test and analysis</p> <ul style="list-style-type: none"> APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted. <ul style="list-style-type: none"> Trigger events (user selectable): <ul style="list-style-type: none"> SDH: SDH alarms and errors, pattern bit error, APS switchover SONET: SONET alarms and errors, pattern bit error, APS switchover Number of switchovers indicated by APS protocol K1/K2 bytes set and displayed <p>Resolution of APS switching time measurement, SDH</p> <ul style="list-style-type: none"> SDH events excluding VC-12 and VC-11 events, LOS (Loss of Signal): 1 μs VC-12 and VC-11 events: 0.5 ms <p>Resolution of APS switching time measurement, SONET</p> <ul style="list-style-type: none"> SONET events excluding VT-1.5 and VT-2 events, LOS (Loss of Signal): 1 μs VT-1.5 and VT-2 events: 0.5 ms
Round Trip Delay (Propagation Time) Measurement	<p>Resolution: 0.1 μs</p> <p>Measured Max. time: 10.0 s</p> <p>Interval: 0.5, 1, 2, 5, 10 s</p>

PDH/DSn Testing Specifications

E1 Common	
Model No.	MU100010A
Test Port	Electrical line interfaces: 2 ports (MU100010A-001) Connector: BNC or RJ48 (selectable)
General	Complies with ITU-T G.703 for 2048 kbps
Impedance	Supported input impedances • 75Ω (unbalanced), 120Ω (balanced), High (>10 × nominal)
Line Code	HDB3 or AMI

E1 Configuration	
Framing	Unframed or Framed: FAS/nFAS, Transmitter: Sa-bits (non-FAS), user-programmable
Transmitter Clock	<ul style="list-style-type: none"> Internal 2.048 Mbps clock accuracy: 4.6 ppm, Clock offset: ±125 ppm (1-ppm steps) Recovered from receiver TTL level external 2.048 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
Receive Signal Rate	<ul style="list-style-type: none"> 2048 kbps ±150 ppm Frequency deviation indication accuracy: ±1 ppm
Receiver Attenuation and Impedance Mode	<p><u>TERMINATE</u></p> <ul style="list-style-type: none"> Up to 40 dB cable attenuation, Nominal impedance <p><u>MONITOR</u></p> <ul style="list-style-type: none"> 20 to 26 dB linear attenuation and up to 6 dB cable attenuation, Nominal impedance 20 to 30 dB linear attenuation, 0 dB cable attenuation, Nominal impedance <p><u>BRIDGED</u></p> <ul style="list-style-type: none"> Up to 40 dB cable attenuation, High impedance
Drop and Insert	Supports drop & insert of one or multiple 64 kbps timeslots (TS) within E1
Alarms	Detected and generated alarms: No signal, AIS, No frame, Distant (RDI) alarm, Pattern sync. loss, No CAS, MFAS, Distant (RDI) MF alarms
Errors	<p>Detected: FAS/nFAS, CRC4, E-bit, Code, Pattern, Pattern slips, Frame slips</p> <p>Generated: FAS bit, FAS word, CRC-4, E-bit, Code, Pattern, Transparent</p> <p>Error insertion</p> <ul style="list-style-type: none"> Manual: 1 to 255 consecutive errors (1 to 16 consecutive FAS word errors) Continuous: 10⁻², 10⁻³, 10⁻⁴, 10⁻⁵, 10⁻⁶, 10⁻⁷ Provoking of G.821, G.826 or M.2100 events (ES, SES etc.) (FAS, Pattern, CRC-4, E-bit) <p>Manual slip insertion: Frame slips, Pattern slips</p>
BER Test Pattern	<p>Pattern generation</p> <ul style="list-style-type: none"> Unframed or Framed: n × 64 kbps in contiguous or non-contiguous channel access <p>Supported test patterns</p> <ul style="list-style-type: none"> PRBS 6, PRBS 7, PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, QRSS 11, QRSS 20 Fox pattern, Fox (CMA 3000), All 0, All 1, Alternating (1:1), (1:3), (1:7), (3:24) User-defined up to 32 bits (Length: 1-bit steps) User-defined up to 2048 bits (Length: 8-bit steps) <p>All patterns can be inverted, except user-defined</p>
CAS	CAS signaling bits can be set.
Tone and Speech Signal Insertion	<p>Tone in one speech channel on one transmitter</p> <ul style="list-style-type: none"> Frequency: 1 Hz to 4 kHz (1-Hz steps) Level: -70 to +3 dBm (1-dBm steps) Artificial speech signal
Speech Decode	64 kbps (ITU-T G.703): A-law according to ITU-T G.711

E1 Results	
Status	<p>Current Information on</p> <ul style="list-style-type: none"> Alarms and errors on monitored line Input level indication Actual bit rate Frequency deviation FAS/non-FAS and CAS bits Traffic overview: Busy/Idle indication from all 31 channels
Time Slot Monitoring	<p>Contents of single time slot including positive/negative peak values.</p> <ul style="list-style-type: none"> Frequency for encoded tone: 1 Hz to 4 kHz (1-Hz steps) Level for encoded tone: -66 to +3 dBm (1-dBm steps)
Statistics	<p>User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h</p> <p>Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation information</p> <p>Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.</p>
Error Performance	<p>G.821, G.826 or M.2100 analysis of PRBS in received signal, or based on CRC-4, E-bit or FAS: ES, SES, BBE (G.826), UAT, EFS, AT % or count.</p> <p>Error performance evaluation for total measurement:</p> <ul style="list-style-type: none"> HR% for user-defined error performance parameter or programmable OK and not-OK limits for FAS, Pattern, CRC-4 or E-bit count or ratio
APS	<p>APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted.</p> <p>Number of switchovers.</p> <p>Trigger events (User selectable): 2 Mbps alarms (LOF or AIS; pattern bit error)</p> <p>Resolution of APS switching time measurement: LOF and AIS: 0.25 ms</p>
Round Trip Delay (Propagation Time) Measurement	<p>Resolution: 1 μs</p> <p>Measured Max. time: 10.0 s</p> <p>Interval: 0.5, 1, 2, 5, 10 s</p>

PDH/DSn Testing Specifications

DS1 Common	
Model No.	MU100010A
Test Port	Electrical line interfaces: 2 ports (MU100010A-001) Connector: Bantam
General	Complies with ANSI T1.102 for 1544 kbps.
Impedance	100Ω or High (10 × nominal; Receiver only) and DSX MON 100Ω ±1%
Line Code	B8ZS, AMI

DS1 Configuration	
Framing	Unframed or Framed, Framed: SF, ESF, J-ESF (J1)
Transmitter Clock	<ul style="list-style-type: none"> Internal 1.544 Mbps clock accuracy: 4.6 ppm, Clock offset: ±125 ppm (1-ppm steps) Recovered from receiver TTL level external 2.048 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
Line Build Out	0, -7.5, -15, -22.5 dB 0 to 133 ft, 133 to 266 ft, 266 to 399 ft, 399 to 533 ft, 533 to 655 ft
Receive Signal Rate	1544 kbps ±150 ppm Frequency deviation indication resolution: ±1 ppm
Receiver Sensitivity	<p><u>DS1 Short Haul</u></p> <ul style="list-style-type: none"> 15 dB linear attenuation, 0 dB cable attenuation, Nominal impedance <p><u>TERMINATE</u></p> <ul style="list-style-type: none"> Up to 36 dB cable attenuation, Nominal impedance <p><u>DSX MONITOR</u></p> <ul style="list-style-type: none"> 15 to 25 dB linear attenuation, Nominal impedance <p><u>BRIDGE</u></p> <ul style="list-style-type: none"> Up to 36 dB cable attenuation, High impedance
Drop and Insert	Drop & Insert of one or multiple 56 kbps or 64 kbps timeslots (TS) within DS1
Alarms	Generated and detected: LOS, OOF, AIS (Blue), RAI (Yellow), LSS
Errors	Generated or detected: Pattern, F-bit, S-bit, Pattern slips, BPV (Code), CRC-6, EXZ Error insertion <ul style="list-style-type: none"> Manual: 1 to 255 consecutive errors Continuous: 10⁻², 10⁻³, 10⁻⁴, 10⁻⁵, 10⁻⁶, 10⁻⁷ For performance: ES, SES
BER Test Pattern	<p>Supported test patterns</p> <ul style="list-style-type: none"> PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, QRSS 20 All 0, All 1, Alternating (1:1), (1:3), (1:7), (3:24), Fox pattern, Fox (CMA 3000) User-defined up to 32 bits (Length: 1-bit steps) User-defined up to 2048 bits (Length: 8-bit steps) <p>All patterns can be inverted, except User-defind</p>
Loopback Code	Supported loopback codes: LLA, LLD, PLA, PLD, ULB, NLA, USR, ACS, DCS, AN1, DN1, AN2, DN2, 100K, USER_INBAND (User-defined FDL/in-band code) Insertion: On/Off
CAS	CAS signaling bits can be set.
Tone and Speech Signal Insertion	Tone in one speech channel on one transmitter <ul style="list-style-type: none"> Frequency: 1 Hz to 4 kHz (1-Hz steps) Level: -70 to +3 dBm (1-dBm steps) Artificial speech signal
Speech Decode	64 kbps or 56 kbps: μ-law

DS1 Results	
Status	Current Information on <ul style="list-style-type: none"> Alarms and errors on monitored line Input level indication Actual bit rate Frequency deviation Contents of one time slot Framing and CAS bits Traffic overview: Busy/Idle indication from all 24 channels
Time Slot Monitoring	Contents of single time slot including positive/negative peak values. <ul style="list-style-type: none"> Frequency for encoded tone: 1 Hz to 4 kHz (1-Hz steps) Level for encoded tone: -66 to +3 dBm (1-dBm steps)
Statistics	User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation information Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.
Error Performance	G.821, G.826, or M.2100 analysis of PRBS in received signal, or based on detected errors: ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)
APS	APS switching time is measured. A switching time exceeding the user-defined threshold is highlighted. Number of switchovers. Trigger events (User selectable): 1.5 Mbps alarms (OOF, AIS; pattern bit error) APS switching time measurement resolution: No frame, AIS: 0.25 ms
Round Trip Delay (Propagation Time) Measurement	Resolution: 1 μs Measured Max. time: 10.0 s Interval: 0.5, 1, 2, 5, 10 s

PDH/DSn Testing Specifications

E3 Common	
Model No.	MU100010A
Test Port	Electrical line interfaces: 2 ports (MU100010A-001) Connector: BNC
General	Complies with ITU-T G.703 for 34368 kbps
Impedance	75Ω
Line Code	HDB3

E3 Configuration	
Framing	Unframed or Framed: Complies with ITU-T G.751 for E3 signals
Transmitter Clock	<ul style="list-style-type: none"> Internal clock accuracy: 4.6 ppm, Clock offset: ±125 ppm (1-ppm steps) Recovered from receiver TTL level external 2.048 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
Receive Signal Rate	34368 kbps ±150 ppm Frequency deviation indication resolution: ±1 ppm
Attenuation and Impedance Mode	<p>TERMINATE</p> <ul style="list-style-type: none"> Up to 12 dB cable attenuation, Nominal impedance <p>MONITOR</p> <ul style="list-style-type: none"> 20 dB linear attenuation and up to 12 dB cable attenuation, Nominal impedance 20 to 30 dB linear attenuation, 0 dB cable attenuation, Nominal impedance
Alarms	Detected and generated alarms: No signal, AIS, No frame, RDI, Pattern sync. loss
Errors	Detected and generated errors: Frame, Code, Pattern, Pattern slip Error insertion <ul style="list-style-type: none"> Manual: 1 to 255 consecutive errors Continuous: 10⁻², 10⁻³, 10⁻⁴, 10⁻⁵, 10⁻⁶, 10⁻⁷ For performance: ES, SES
BER Test Pattern	Pattern Generation and Detection, Supported test patterns <ul style="list-style-type: none"> PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23 Fox pattern, Fox (CMA 3000), All 0, All 1, Alternating 1:1, Alternating 1:3, Alternating 1:7, Alternating 3:24 User-defined up to 32 bits (Length: 1-bit steps) User-defined up to 2048 bits (Length: 8-bit steps) All patterns can be inverted, except user-defined

E3 Results	
Status	Current Information on <ul style="list-style-type: none"> Alarms and errors on monitored line Input level indication Actual bit rate Frequency deviation
Statistics	User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation information Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.
Error Performance	G.826/M.2100 analysis of received signal, or based on detected errors ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)
Round Trip Delay (Propagation Time) Measurement	Resolution: 1 μs Measured Max. time: 10.0 s Interval: 0.5, 1, 2, 5, 10 s

DS3 Common	
Model No.	MU100010A
Test Port	Electrical line interfaces: 2 ports (MU100010A-001) Connector: BNC
General	Complies with ANSI for 44736 kbps
Impedance	75Ω
Line Code	B3ZS

DS3 Configuration	
Framing	Unframed or Framed, Framed: C-bit parity, M13 in accordance with ANSI T1.107
Transmitter Clock	<ul style="list-style-type: none"> Internal clock accuracy: 4.6 ppm, Clock offset: ±125 ppm (1-ppm steps) Recovered from receiver TTL level external 2.048 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
Line Build Out	0 ft, 225 ft
Receive Signal Rate	44736 kbps ±150 ppm Frequency deviation indication resolution: ±1 ppm
Attenuation and Impedance Mode	<p>TERMINATE</p> <ul style="list-style-type: none"> Up to 12 dB cable attenuation, Nominal impedance <p>MONITOR</p> <ul style="list-style-type: none"> 20 dB linear attenuation and up to 12 dB cable attenuation, Nominal impedance 20 to 30 dB linear attenuation, 0 dB cable attenuation, Nominal impedance
Alarms	Detected and generated alarms: LOS, LOF, AIS (Blue), RAI (Yellow), DS3 idle, LSS

PDH/DSn Testing Specifications

Errors	Detected and generated errors: Pattern, C-bit, F-bit, P-bit, Code (BPV), FEBE (detect only), EXZ (detect only) Error insertion <ul style="list-style-type: none"> Manual: 1 to 255 consecutive errors Continuous: 10^{-2}, 10^{-3}, 10^{-4}, 10^{-5}, 10^{-6}, 10^{-7}
BER Test Pattern	Pattern generation and detection, Supported test patterns <ul style="list-style-type: none"> PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, QRSS 20 Fox pattern, Fox (CMA 3000), All 0, All 1, Alternating 1:1, Alternating 1:3, Alternating 1:7, Alternating 3:24 User-defined up to 32 bits (Length: 1-bit steps) User-defined up to 2048 bits (Length: 8-bit steps) All patterns can be inverted, except user-defined
Loopback Code	Supports FEAC and C-bits loopback (ANSI T1.404 & T1.107a)

DS3 Results	
Status	Current information on <ul style="list-style-type: none"> Alarms and errors on monitored line Input level indication Actual bit rate Frequency deviation
Statistics	User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.
Error Performance	G.826/M.2100 analysis of received signal, or based on detected errors ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)
Round Trip Delay (Propagation Time) Measurement	Resolution: 1 μ s Measured Max. time: 10.0 s Interval: 0.5, 1, 2, 5, 10 s

E4 Common	
Model No.	MU100010A
Test Port	Electrical line interfaces: 2 ports (MU100010A-001) Connector: BNC
General	Complies with ITU-T G.703 for 139264 kbps interfaces
Impedance	75 Ω
Line Code	CMI

E4 Configuration	
Framing	Unframed or Framed: Complies with ITU-T G.751 for E4 signals
Transmitter Clock	<ul style="list-style-type: none"> Internal clock accuracy: 4.6 ppm, Clock offset: ± 125 ppm (1-ppm steps) Recovered from receiver TTL level external 2.048 MHz clock SETS (E1: 2.048 Mbps), BITS (DS1: 1.544 Mbps)
Receive Signal Rate	139264 kbps ± 150 ppm Frequency deviation indication resolution: ± 1 ppm
Attenuation and Impedance Mode	TERMINATE <ul style="list-style-type: none"> Up to 12 dB cable attenuation, Nominal impedance MONITOR <ul style="list-style-type: none"> 20 dB linear attenuation and up to 12 dB cable attenuation, Nominal impedance
Alarms	Detected and generated alarms: No signal, AIS, No frame, RDI, Pattern sync. loss
Errors	Detected and generated errors: Frame, Pattern error, Pattern slips Error insertion <ul style="list-style-type: none"> Manual: 1 to 255 consecutive errors Continuous: 10^{-2}, 10^{-3}, 10^{-4}, 10^{-5}, 10^{-6}, 10^{-7} For performance: ES, SES
BER Test Pattern	Pattern generation and detection, Supported test patterns <ul style="list-style-type: none"> PRBS 9, PRBS 11, PRBS 15, PRBS 20, PRBS 23, PRBS 29, PRBS 31, QRSS 20 All 0, All 1, Alternating 1:1, Alternating 1:3, Alternating 1:7, Alternating 3:24 User-defined up to 32 bits (Length: 1-bit steps) User-defined up to 2048 bits (Length: 8-bit steps) All patterns can be inverted, except user-defined

E4 Results	
Status	Current information on <ul style="list-style-type: none"> Alarms and errors on monitored line Input level indication Actual bit rate Frequency deviation
Statistics	User-defined measurement resolution: 1, 2, 5, 10, 15, 30 s, 1, 5, 10, 15, 30 min, 1, 2, 4, 6, 12 h Logged information: Alarms (seconds and ratio), Errors (count or count and ratio), Frequency deviation Event log: Major measurement events incl. errors and alarms are logged with 1-second resolution.
Error Performance	G.826/M.2100 analysis of received signal, or based on detected errors ES, SES, ALS, UAT, AVT, EFS, BBE (G.826)
Round Trip Delay (Propagation Time) Measurement	Resolution: 1 μ s Measured Max. time: 10.0 s Interval: 0.5, 1, 2, 5, 10 s

Device Testing Specification

Device Test	
Model No.	MU100011A
Interface Type	QSFP+, SFP28, CFP4, QSFP28
Supported Bit Rate	25.78 Gbps (25 GigE), 41.25 Gbps (40 GigE), 103.12 Gbps (100 GigE), 43.01 Gbps (OTU3), 44.57 Gbps (OTU3e1), 44.58 Gbps (OTU3e2), 111.80 Gbps (OUT4)
Lane Select	1 Lane (SFP28), 4 Lane (QSFP+, CFP4*, QSFP28*), 20 Lane (CFP4, QSFP28)
Timing Source	Internal clock, External clock
Test Pattern	PRBS 7, PRBS 9, PRBS 15, PRBS 23, PRBS 31, Logic Inverted
Frequency Offset	±200 ppm (0.1 ppm step)
Error Insertion	Item: Bit error Insertion timing: Single
Monitor	Signal level: dBm Frequency: Hz, ppm LOS, LSS, CDR lock, Bit error
Measurement	Bit error, Frequency

*: The CFP4 and QSFP28 can select test patterns for each lane.

Optical Transceivers Interface List

MU10010A	MU10011A	Model/ Order No.	Name	Form Factor	100 Meg Ethernet	156 Meg STM-1	614 Meg CPRI	622 Meg STM-4	768 Meg OBSAI	1GFC	1.23 Gig CPRI	1.25 Gig Ethernet	1.54 Gig OBSAI	2GFC	2.46 Gig CPRI	2.488 Gig STM-16	2.67 Gig OTU1	3.07 Gig CPRI/OBSAI	4GFC	4.92 Gig CPRI	6.14 Gig CPRI/OBSAI	8GFC	9.83 Gig CPRI	9.95 Gig STM-64	10.1 Gig CPRI	10.3 Gig Ethernet	10GFC	10.7 Gig OTU2	11.05 Gig OTU1e	11.09 Gig OTU2e	11.27 Gig OTU1f	11.3 Gig OTU2f	16GFC	25G Ethernet	40G Ethernet	40G OTN	100G Ethernet	100G OTN					
✓	✓	G0332A	100M FX 1310 nm MM SFP	SFP	1310 nm, MM, 2 km																																						
✓	✓	G0319A	Up to 2.7G 1310 nm 15 km SFP	SFP							1310 nm, SM, 15 km																																
✓	✓	G0320A	Up to 2.7G 1310 nm 40 km SFP	SFP							1310 nm, SM, 40 km																																
✓	✓	G0321A	Up to 2.7G 1550 nm 80 km SFP	SFP							1550 nm, SM, 80 km																																
✓	✓	G0328A	1G/2G/4G FC 850 nm SFP	SFP							850 nm, MM, 0.5 km																																
✓	✓	G0322A	1G/2G/4G FC 1310 nm SFP	SFP							1310 nm, SM, 10 km																																
✓	✓	G0323A	1G/2G/4G FC 1550 nm SFP	SFP							1550 nm, SM, 40 km																																
✓	✓	G0315A	10G LR/LW 1310 nm SFP+	SFP+																																							
✓	✓	G0316A	10G ER/EW 1550 nm 40 km SFP+	SFP+																																							
✓	✓	G0318A	10G ZR/ZW 1550 nm 80 km SFP+	SFP+																																							
✓	✓	G0329A	10G LR 1310 nm SFP+	SFP+							1310 nm, SM, 10 km																																
✓	✓	G0356A	8G FC/10G SR 850 nm SFP+	SFP+																																							
	✓	G0386A	16GFC SR 850 nm SFP+	SFP+																																							
	✓	G0387A	16GFC LR 1310 nm SFP+	SFP+																																							
	✓	G0388A	25G SR 850 nm SFP28	SFP28																																							
	✓	G0389A	25G LR 1310 nm SFP28	SFP28																																							
	✓	G0359A	40G SR4 850 nm QSFP+	QSFP+																																							
	✓	G0334A	40G LR4 1310 nm QSFP+	QSFP+																																							
	✓	G0366A	100G SR4 850 nm QSFP28	QSFP28																																							
	✓	G0364A	100G LR4 1310 nm QSFP28	QSFP28																																							
	✓	G0365A	100G LR4 Dual Rate 1310 nm QSFP28	QSFP28																																							
	✓	G0369A	100G LR4 Dual Rate 1310 nm CFP4	CFP4																																							

Optical Transceivers Specification

Model/Order No.	Description (Approx. Distance)	Max. Input Power	Input Sensitivity	Input Wavelength	Output Power	Output Wavelength	Loop Back
G0332A 100M FX 1310 nm MM SFP	100BASE - FX 1310 nm multi mode (2 km)	-14 dBm	-31 dBm	1270 nm to 1600 nm	-20 to -15 dBm	1280 nm to 1380 nm	OK
G0319A Up to 2.7G 1310 nm 15 km SFP	STM-1/4/16 short haul 1310 nm single mode (15 km)	0 dBm	-18 dBm	1270 nm to 1580 nm	-5 to 0 dBm	1260 nm to 1360 nm	OK
G0320A Up to 2.7G 1310 nm 40 km SFP	STM-1/4/16 long haul 1310 nm single mode (40 km)	-9 dBm	-27 dBm	1270 nm to 1580 nm	-2 to +3 dBm	1280 nm to 1335 nm	>12 dB ATT
G0321A Up to 2.7G 1550 nm 80 km SFP	STM-1/4/16 long haul 1550 nm single mode (80 km)	-9 dBm	-28 dBm	1270 nm to 1580 nm	-2 to +3 dBm	1500 nm to 1580 nm	>12 dB ATT
G0328A 1G/2G/4G FC 850 nm SFP	1GFC, 2GFC, 4GFC 850 nm multi mode (0.5 km)	-3 dBm	-15 dBm	830 nm to 860 nm	-9 to 0 dBm	830 nm to 860 nm	>3 dB ATT
G0322A 1G/2G/4G FC 1310 nm SFP	1GFC, 2GFC, 4GFC 1310 nm single mode (10 km)	-3 dBm	-18 dBm	1260 nm to 1360 nm	-8 to 0 dBm	1260 nm to 1360 nm	>3 dB ATT
G0323A 1G/2G/4G FC 1550 nm SFP	1GFC, 2GFC, 4GFC 1550 nm single mode (40 km)	-3 dBm	-18 dBm	1470 nm to 1600 nm	0 to +5 dBm	1510 nm to 1590 nm	>8 dB ATT
G0315A 10G LR/LW 1310 nm SFP+	10GBASE - LR 1310 nm single mode (10 km)	+0.5 dBm	-14.4 dBm	1260 nm to 1565 nm	-6 to -1 dBm	1290 nm to 1330 nm	OK
G0316A 10G ER/EW 1550 nm 40 km SFP+	10GBASE - ER 1550 nm single mode (40 km)	-1 dBm	-15.8 dBm	1260 nm to 1565 nm	-3 to +3 dBm	1530 nm to 1560 nm	>4 dB ATT
G0318A 10G ZR/ZW 1550 nm 80 km SFP+	10GBASE - ER 1550 nm single mode (80 km)	-8 dBm	-22 dBm	1260 nm to 1565 nm	0 to +5 dBm	1525 nm to 1565 nm	>13 dB ATT
G0329A 10G LR 1310 nm SFP+	10GBASE - LR 1310 nm single mode (10 km)	+0.5 dBm	-14 dBm	1260 nm to 1355 nm	-8.2 to +0.5 dBm	1260 nm to 1355 nm	OK
G0356A 8G FC/10G SR 850 nm SFP+	8GFC, 10GFC, 10GBASE - SR 850 nm multi mode (0.3 km)	-1 dBm	-11.1 dBm	840 nm to 860 nm	-7.3 to -1 dBm	840 nm to 860 nm	OK
G0386A 16GFC SR 850 nm SFP+	16GFC 850 nm multi mode (0.035 km)	0 dBm	-10.5 dBm	840 nm to 860 nm	-7.5 dBm ~	840 nm to 860 nm	OK
G0387A 16GFC LR 1310 nm SFP+	16GFC 1310 nm single mode (10 km)	+2 dBm	-12 dBm	1295 nm to 1325 nm	-5 to +2 dBm	1295 nm to 1325 nm	OK
G0388A 25G SR 850 nm SFP28	25GBASE - SR 850 nm multi mode (0.1 km)	+2.4 dBm	-10.3 dBm	840 nm to 860 nm	-8.4 to +2.4 dBm	840 nm to 860 nm	OK
G0389A 25G LR 1310 nm SFP28	25GBASE - LR 1310 nm single mode (10 km)	+2 dBm	-13.3 dBm	1260 nm to 1350 nm	-7 to +2 dBm	1295 nm to 1325 nm	OK
G0359A 40G SR4 850 nm QSFP+	40GBASE - SR4 850 nm multi mode (0.1 km)	+2.4 dBm (per Lane)	-9.9 dBm	840 nm to 860 nm	-8 to +2.4 dBm	840 nm to 860 nm	OK
G0334A 40G LR4 1310nm QSFP+	40G Ethernet/OTN 1310 nm single mode (10 km)	+2.3 dBm (per Lane)	-11.5 dBm (per Lane)	1264.5 nm to 1277.5 nm 1284.5 nm to 1297.5 nm 1304.5 nm to 1317.5 nm 1324.5 nm to 1337.5 nm	+8.3 dBm (max.) (Total) -2 to +2.3 dBm (per Lane)	1264.5 nm to 1277.5 nm 1284.5 nm to 1297.5 nm 1304.5 nm to 1317.5 nm 1324.5 nm to 1337.5 nm	OK
G0366A 100G SR4 850 nm QSFP28	100G Ethernet 850 nm multi mode (0.1 km)	+2.4 dBm (per Lane)	-9.9 dBm (per Lane)	840 nm to 860 nm	+8.9 dBm (max.) (Total) -9.1 to +2.4 dBm (per Lane)	840 nm to 860 nm	OK
G0364A 100G LR4 1310 nm QSFP28	100G Ethernet 1310 nm single mode (10 km)	+4.5 dBm (per Lane)	-8.6 dBm (per Lane)	1294.53 nm to 1296.59 nm 1299.02 nm to 1301.09 nm 1303.54 nm to 1305.63 nm 1308.09 nm to 1310.19 nm	+10.5 dBm (max.) (Total) -4.3 to +4.5 dBm (per Lane)	1294.53 nm to 1296.59 nm 1299.02 nm to 1301.09 nm 1303.54 nm to 1305.63 nm 1308.09 nm to 1310.19 nm	OK
G0365A 100G LR4 Dual Rate 1310 nm QSFP28	100G Ethernet /OTN 1310 nm single mode (10 km)	+4 dBm (per Lane)	-8.4 dBm (per Lane)	1294.53 nm to 1296.59 nm 1299.02 nm to 1301.09 nm 1303.54 nm to 1305.63 nm 1308.09 nm to 1310.19 nm	+10 dBm (max.) (Total) -0.6 to +4 dBm (per Lane)	1294.53 nm to 1296.59 nm 1299.02 nm to 1301.09 nm 1303.54 nm to 1305.63 nm 1308.09 nm to 1310.19 nm	OK
G0369A 100G LR4 Dual Rate 1310 nm CFP4	100G Ethernet /OTN 1310 nm single mode (10 km)	+4.5 dBm (per Lane)	-8.6 dBm (per Lane)	1294.53 nm to 1296.59 nm 1299.02 nm to 1301.09 nm 1303.54 nm to 1305.63 nm 1308.09 nm to 1310.19 nm	+10.5 dBm (max.) (Total) -4.3 to +4.5 dBm (per Lane)	1294.53 nm to 1296.59 nm 1299.02 nm to 1301.09 nm 1303.54 nm to 1305.63 nm 1308.09 nm to 1310.19 nm	OK

Ordering Information

Please specify the model/order number, name and quantity when ordering.
The names listed in the chart below are Order Names. The actual name of the item may differ from the Order Name.

Mainframe

Model/Order No.	Name
MT1000A	Network Master Pro
Standard Accessories	
MT1000A-006*1	High Power Supply: Installed
	Line Cord*2: 1 pc
B0690A	Softbag: 1 pc
B0728A*3	Rear Panel kit: 1 pc
G0385A*4	High Power AC Adaptor: 1 pc
G0310A	Li-ion Battery: 1 pc
Z1746A	Stylus: 1 pc
Z1747A*5	Carrying Strap: 1 pc
Z1748A*6	Handle: 1 pc
Z1817A*7	Utilities ROM: 1 pc
Options	
MT1000A-003*8	Connectivity for WLAN/Bluetooth
MT1000A-005*9	AUX I/O

Model/Order No.	Name
Optional Accessories	
B0691B*10	Hard Case
B0720A	Rear Panel
B0729A*11	Screw 1U
B0730A*11	Screw 2U
B0731A*11	Screw 3U
B0732A*12	Screw Kit
G0382A*13	Autofocus Video Inspection Probe
G0306B*13	Video Inspection Probe
G0309A*4	AC Adapter
G0324A	Battery Charger
G0325A	GPS Receiver
J1569B	Car 12 Vdc Adapter
J1667A*14	GPB-USB Converter
Z1821A*15	Utilities in USB Stick

Module Configuration*16

The diagram illustrates three module configurations:

- 1 Module:** Transport Mainframe, OTDR Mainframe, CPRI RF Mainframe.
- 2 Modules:** Transport Mainframe, OTDR Mainframe, GPS/Rb Mainframe, CPRI RF Mainframe, CPRI RF Mainframe, OTDR Mainframe.
- 3 Modules:** Transport Mainframe, OTDR Mainframe, GPS/Rb Mainframe, CPRI RF Mainframe.

Legend for Module Types:

- Mainframe: Network Master Pro MT1000A
- Transport: Transport Module (10G Multirate MU100010A, 100G Multirate MU100011A)
- OTDR: OTDR Module (1310/1550 nm SMF MU100020A, 1310/1550/850/1300 nm SMF/MMF MU100021A, 1310/1550/1625 nm SMF MU100022A)
- CPRI RF: CPRI RF Module MU100040B
- GPS/Rb: High Performance GPS Disciplined Oscillator MU100090A

*1: The presence of the MT1000A-006 option can be recognized at the top right of the front panel. To retrofit to the already shipped item, please contact us.



Without MT1000A-006 With in MT1000A-006

- *2: One line cord is attached to the area to shipment.
- *3: Composed of B0720A, B0729A, B0730A and B0731A. Refer to Module Composition for the module combination.
- *4: The MT1000A with MT1000A-006 can be used. Use the AC adapter when using the MT1000A without MT1000A-006 installed.
- *5: Shoulder strap for MT1000A.
- *6: Hand strap for MT1000A.
- *7: This DVD includes PDF files and formatting tools of each product's instruction manual (such as W3933AE, W3810AE, W3736AE, W3946AE).
- *8: Available for certified countries and regions including USA, Canada, Japan and EU countries. Please visit the Anritsu web site for updated information.
- *9: MT1000A-005 is required for MU100090A. To retrofit to the already shipped item, please contact us.

- *10: Can use module 1 to 2 in combination
- *11: Includes 4 bolts of same length
- *12: Includes B0729A, B0730A and B0731A
- *13: This fiberscope uses the VIP function in the MT1000A Utility menu. Different tip types are used by the G0382A and G0306B.



G0382A

G0306B

- *14: J1667A is required for SCPI remote control via GPB
- *15: Include MT1000A Operation Manual and the Remote Script Manual.
- *16: Any modular combination as shown in a figure.
- *17: Required if the transport module is not used rear cover.

Ordering Information

Measurement Module

10G Multirate Module MU100010A

Model/Order No.	Name
MU100010A	10G Multirate Module
Standard accessories	
W3935AE	MT1000A Transport Module Quick Reference Guide: 1 pc
B0692A*1	ESD Box (for optical modules)

*1: Up to four SFP+/SFPS can be stored.

Module Options*2

Model/Order No.	Name
Low Rate	
MU100010A-001*3	Up to 2.7G Dual Channel
Ethernet	
MU100010A-011	Ethernet 10G Single Channel
MU100010A-012	Ethernet 10G Dual Channel
MU100010A-020*4	TCP Throughput
OTN	
MU100010A-051	OTN 10G Single Channel
MU100010A-052	OTN 10G Dual Channel
MU100010A-061*5	ODU Multiplexing
MU100010A-062*5	ODU Flex

Model/Order No.	Name
CPRI/OBSAI	
MU100010A-071	CPRI/OBSAI Up to 5G Dual Channel
MU100010A-072	CPRI/OBSAI 6G to 10G Single Channel
MU100010A-073	CPRI/OBSAI 6G to 10G Dual Channel
Fibre Channel	
MU100010A-002	FC 1G 2G 4G Dual Channel
MU100010A-091	FC 8G 10G Single Channel
MU100010A-092	FC 8G 10G Dual Channel
SDH/SONET	
MU100010A-081	STM-64 OC-192 Single Channel
MU100010A-082	STM-64 OC-192 Dual Channel

*2: These options can be retrofitted.

The Model/Order No. of retrofit options is "-3**".

Example

MU100010A-001 Up to 2.7G Dual Channel becomes MU100010A-301 Up to 2.7G Dual Channel Retrofit.

In addition, specify one of the following media along with the required option.

Z1849A: DVD-ROM for Retrofit Options

Z1850A: USB Stick for Retrofit Options

*3: Includes OTN (OTU1), Ethernet (10 Mbps, 100 Mbps, 1 Gbps), SDH up to STM-16, SONET up to OC-48, PDH (E1, E3, E4), and DS_n (DS1, DS3)

*4: Requires that at least one of the following options is installed: MU100010A-001, MU100010A-011, MU100010A-012

*5: Requires that at least one of the following options is installed: MU100010A-001, MU100010A-051, MU100010A-052

Correspondence Ports by MU100010A Options

	Standard (Bit Rate)	Interface	The Number Of Correspondence Ports by MU100010A Options													
			001	002	011	012	051	052	071	072	073	081	082	091	092	
Ethernet	10/100/1000M (Electrical)	RJ45	2 Port													
	100BASE-FX (125M)	SFP	2 Port													
	1000BASE-X (1.25G)	SFP+														
	10GBASE-XR (10.3125G) 10GBASE-XW (9.953G)	SFP+			1 Port	2 Port										
OTN	OTU1 (2.666G)	SFP	2 Port													
	OTU2 (10.709G)															
	OTU1e (11.049G)															
	OTU2e (11.096G)	SFP+					1 Port	2 Port								
	OTU1f (11.270G) OTU2f (11.318G)															
CPRI/OBSAI	Option 1 (614.4M) Option 2 (1228.8M) Option 3 (2457.6M) Option 4 (3073.0M) Option 5 (4915.2M)	SFP								2 Port						
	Option 6 (6144.0M) Option 7 (9830.4M) Option 8 (10137.6M)	SFP+									1 Port	2 Port				
Fibre Channel	1GFC (1.063G) 2GFC (2.125G) 4GFC (4.250G)	SFP		2 Port												
	8GFC (8.500G) 10GFC (10.520G)	SFP+											1 Port	2 Port		
SDH/SONET	STM-1e/STS-3 (155.52M)	BNC	2 Port													
	STM-1/OC-3 (155.52M) STM-4/OC-12 (622.08M)	SFP	2 Port													
	STM-64/OC-192 (9.953G)	SFP+										1 Port	2 Port			
PDH	E1 (2.048M)	RJ48, BNC	2 Port													
	E3 (34.368M) E4 (139.264M)	BNC	2 Port													
DS _n	DS1 (1.544M)	RTT Bantam	2 Port													
	DS3 (44.736M)	BNC	2 Port													

Ordering Information

Measurement Module

100G Multirate Module MU100011A

Model/Order No.	Name
MU100011A*1	100G Multirate Module
Standard accessories	
W3935AE	MT1000A Transport Module Quick Reference Guide: 1 pc

*1: MT1000A-006 is required for MU100011A.

Module Options

Model/Order No.	Name
Standard	
MU100011A-001*2	Up to 10G Single Channel
MU100011A-003*2	Up to 10G Dual Channel
Ethernet	
MU100011A-013	Ethernet 40G Single Channel
MU100011A-015	Ethernet 100G Single Channel
MU100011A-017	Ethernet 25G Single Channel
MU100011A-020*3	TCP Throughput
MU100011A-023*4	RS-FEC for 100GBASE-SR4

Model/Order No.	Name
OTN	
MU100011A-053	OTN 40G Single Channel
MU100011A-055	OTN 100G Single Channel
MU100011A-062*5	ODU Flex
MU100011A-063*5	ODU Multiplexing/Multi Stage
Fibre Channel	
MU100011A-004	Up to 10G FC Single Channel
MU100011A-005	Up to 10G FC Dual Channel
MU100011A-091	FC 16G Single Channel
CPRI/OBSAI	
MU100011A-071	CPRI/OBSAI Up to 10G Single Channel
MU100011A-072	CPRI/OBSAI Up to 10G Dual Channel
SDH/SONET	
MU100011A-083*6	STM-256/OC-768 Client Signal

*2: Only one of these option can be installed.

Included OTN(OTU1, OTU1e, OTU1f, OTU2, OTU2e, OTU2f), Ethernet up to 10 Gbps, SDH up to STM-64 and SONET up to OC-192.

*3: Requires that at least one of the following option is installed:

MU100011A-001, MU100011A-003

*4: Requires to MU100011A-015

*5: Requires that at least one of the following option is installed:

MU100011A-001, MU100011A-003, MU100011A-053, MU100011A-054

*6: MU100011A does not have a physical interface of the option.

The option is required for client signal mapped in the OTN.

Correspondence Ports by MU100011A Options

Standard (Bit Rate)	Interface	The Number Of Correspondence Ports by MU100011A Options													
		001	003	013	015	017	053	055	004	005	091	071	072		
Ethernet	10/100/1000M (Electrical)	RJ45	1 Port	2 Port											
	100BASE-FX (125M)	SFP	1 Port	2 Port											
	1000BASE-X (1.25G)	SFP	1 Port	2 Port											
	10GBASE-XR (10.3125G)	SFP+	1 Port	2 Port											
	10GBASE-XW (9.953G)	SFP+													
	25GBASE-XX (25.781G)	SFP28						1 Port							
OTN	40GBASE-XX4 (41.250G)	QSFP+			1 Port										
	100GBASE-XX4 (103.125G)	CFP4, QSFP28					1 Port								
	OTU1 (2.666G)	SFP	1 Port	2 Port											
	OTU2 (10.709G)	SFP+	1 Port	2 Port											
	OTU1e (11.049G)														
	OTU2e (11.096G)														
OTU1f (11.270G)															
OTU2f (11.318G)															
OTU3 (43.018G)	QSFP+						1 Port								
OTU3e1 (44.571G)															
OTU3e2 (44.583G)															
OTU4 (111.810G)	CFP4, QSFP28							1 Port							
CPRI/OBSAI	Option 1 (614.4M)	SFP												1 Port	2 Port
	Option 2 (1228.8M)														
	Option 3 (2457.6M)														
	Option 4 (3073.0M)														
	Option 5 (4915.2M)														
Option 6 (6144.0M)	SFP+												1 Port	2 Port	
Option 7 (9830.4M)															
Option 8 (10137.6M)															
Fibre Channel	1GFC (1.063G)	SFP													
	2GFC (2.125G)														
	4GFC (4.250G)														
	8GFC (8.500G)														
10GFC (10.520G)	SFP+														
16GFC (14.025G)	SFP+													1 Port	
SDH/SONET	STM-1/OC-3 (155.52M)	SFP	1 Port	2 Port											
	STM-4/OC-12 (622.08M)														
	STM-64/OC-192 (9.953G)														

Ordering Information

GPS Disciplined Oscillator MU100090A

Model/Order No.	Name
MU100090A*1	High Performance GPS Disciplined Oscillator
Standard accessories	
J1705A	AUX Conversion Adaptor
J1706A	GPS Antenna
J1710A	BNC Cable (20 cm) × 2

*1: Excellent Eco Product non-compliant.
MT1000A-005 is required for MU100090A.

Optional Accessories

Model/Order No.	Name
Operation Manuals	
W3933AE	MT1000A Transport Module Operation Manual
W3736AE	MT1000A/MT1100A Remote Scripting Operation Manual
Optical Module	
G0332A	100M FX 1310 nm MM SFP
G0319A	Up to 2.7G 1310 nm 15 km SFP
G0320A	Up to 2.7G 1310 nm 40 km SFP
G0321A	Up to 2.7G 1550 nm 80 km SFP
G0328A	1G/2G/4G FC 850 nm SFP
G0322A	1G/2G/4G FC 1310 nm SFP
G0323A	1G/2G/4G FC 1550 nm SFP
G0315A	10G LR/LW 1310 nm SFP+
G0316A	10G ER/EW 1550 nm 40 km SFP+
G0318A	10G ZR/ZW 1550 nm 80 km SFP+
G0329A	10G LR 1310 nm SFP+
G0356A	8G FC/10G SR 850 nm SFP+
G0386A	16GFC SR 850 nm SFP+
G0387A	16GFC LR 1310 nm SFP+
G0388A	25G SR 850 nm SFP28
G0389A	25G LR 1310 nm SFP28
G0359A	40G SR4 850 nm QSFP+
G0334A	40G LR4 1310 nm QSFP+
G0366A	100G SR4 850 nm QSFP28
G0364A	100G LR4 1310 nm QSFP28
G0365A	100G LR4 Dual Rate 1310 nm QSFP28
G0369A	100G LR4 Dual Rate 1310 nm CFP4

Model/Order No.	Name
Cables	
J1571A	Optical Cable SM LC/PC to SC/PC 3 m
J1575A	Optical Cable SM LC/PC to FC/PC 3 m
J1579A	Optical Cable SM LC/PC to LC/PC 3 m
J1581A	Optical Cable MM LC/PC to LC/PC 3 m
J1583A	Optical Attenuator 10 dB LC/PC to LC/PC
J1584A	RJ45 Cable 3 m
J1585A*2	RJ48 to Crocodile Clips Cable 3 m
J1586A*2	RJ48 to Crocodile Clips Cable 20 dB ATT 3 m
J1588A*3	BNC Cable 2.5 m
J1589A*3	BNC to 1.6/5.6 Cable 2.5 m
J1591A*2	RJ48 to Two 3-pin Banana Plug Cable 2.5 m
J1597A*2	RJ48 Balanced PDH Cable Crossed 3 m
J1598A*4	Bantam Cable 3 m
J1710A*5	BNC Cable 0.2 m
J0127B*5	COAXIAL CORD, 2.0 M

*2: E1 interface cable.

*3: E1, E3, E4, DS3, STM-1e, STS-3 interface cable. Impedance: 75Ω

*4: DS1 interface cable.

*5: 50Ω impedance cable for MU100090A and main-frame external clock input connector

Maintenance Service

Model/Order No.	Name	Supported Modules
MT1000A-ES210	2 Years Extended Warranty Service	MT1000A
MT1000A-ES310	3 Years Extended Warranty Service	
MT1000A-ES510	5 Years Extended Warranty Service	
MU100010A-ES210	2 Years Extended Warranty Service	MU100010A
MU100010A-ES310	3 Years Extended Warranty Service	
MU100010A-ES510	5 Years Extended Warranty Service	
MU100011A-ES210	2 Years Extended Warranty Service	MU100011A
MU100011A-ES310	3 Years Extended Warranty Service	
MU100011A-ES510	5 Years Extended Warranty Service	
MU100090A-ES210	2 Years Extended Warranty Service	MU100090A
MU100090A-ES310	3 Years Extended Warranty Service	
MU100090A-ES510	5 Years Extended Warranty Service	

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